

EPROM NAME TEST

There is storage space inside of the Xilinx EPROM to store the name of the board. On initial programming this should be set FF FF FF FF. If the chip has been programmed before it will be set to FE 00 XX XX where XX is the name of the board.

ERROR File

Readback file the readback name the program has detected as invalid.

```
PROM Name  
fe 00 00 48 ← valid name
```

Scope Debugging

- 1) Check JTAG is working (see "JTAG TEST", test1.pdf).
- 2) If JTAG is working and the name is still invalid see S. Durkin