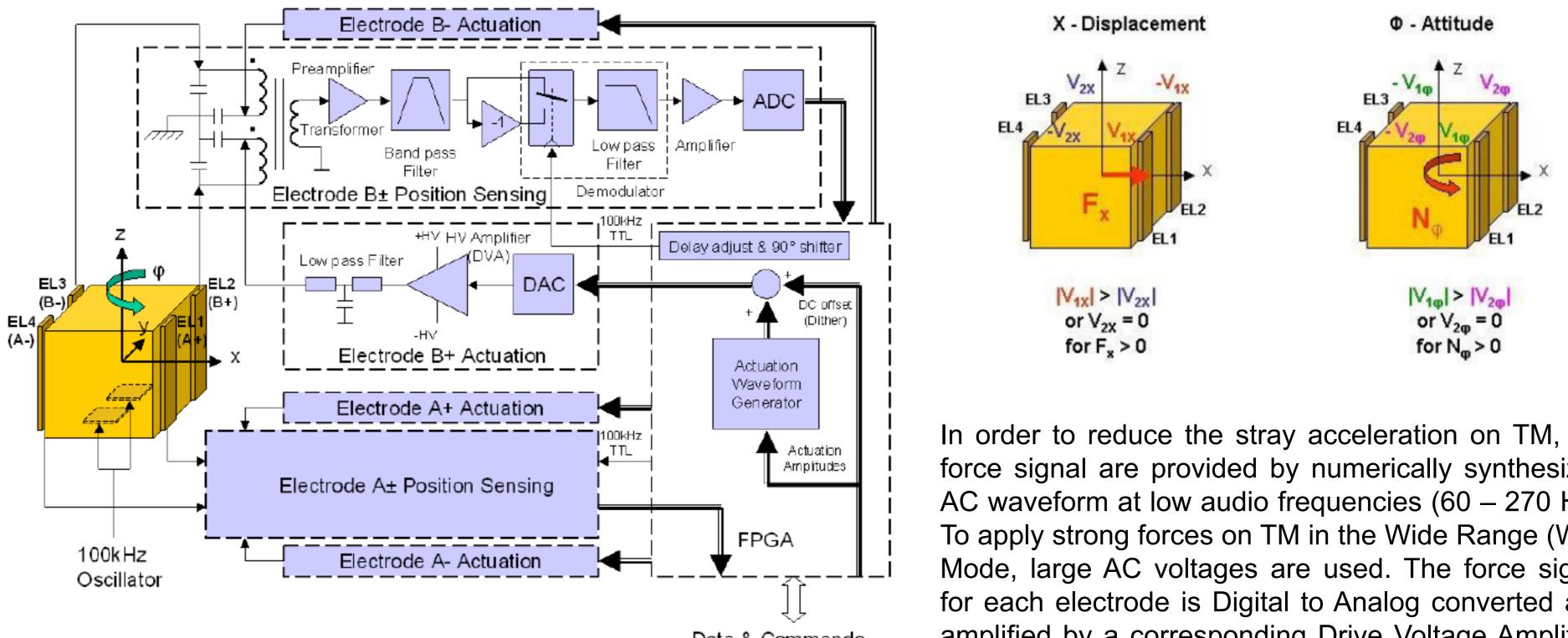
ETHZURICH

LISA GRS FEE test campaign at ETH Zürich

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LISA GRS Front End Electronics

The TM is electrostatically suspended between surrounding electrodes and its motion along x or φ causes an imbalance in capacitance, i.e. in currents in primary windings of both transformer bridges (between electrodes A+/- and B+/-). The imbalance in currents reflects in the level and sign of the current flowing in the secondary winding of each transformer. The corresponding preamplifier

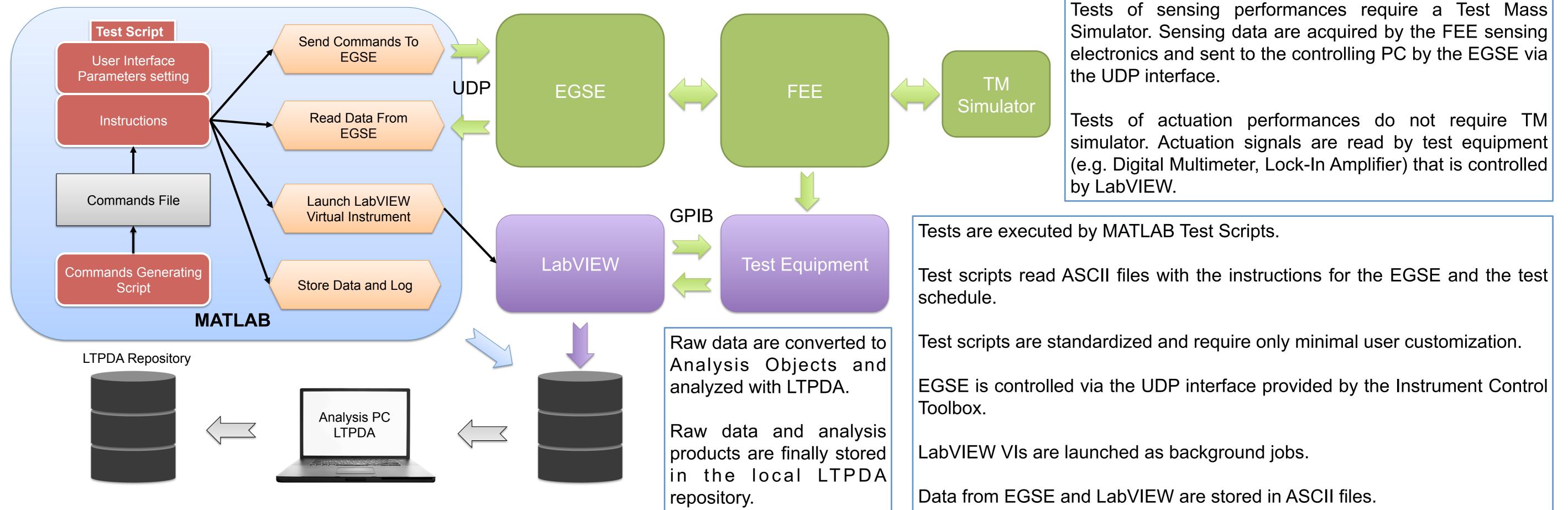


detects the current and converts it to a voltage.

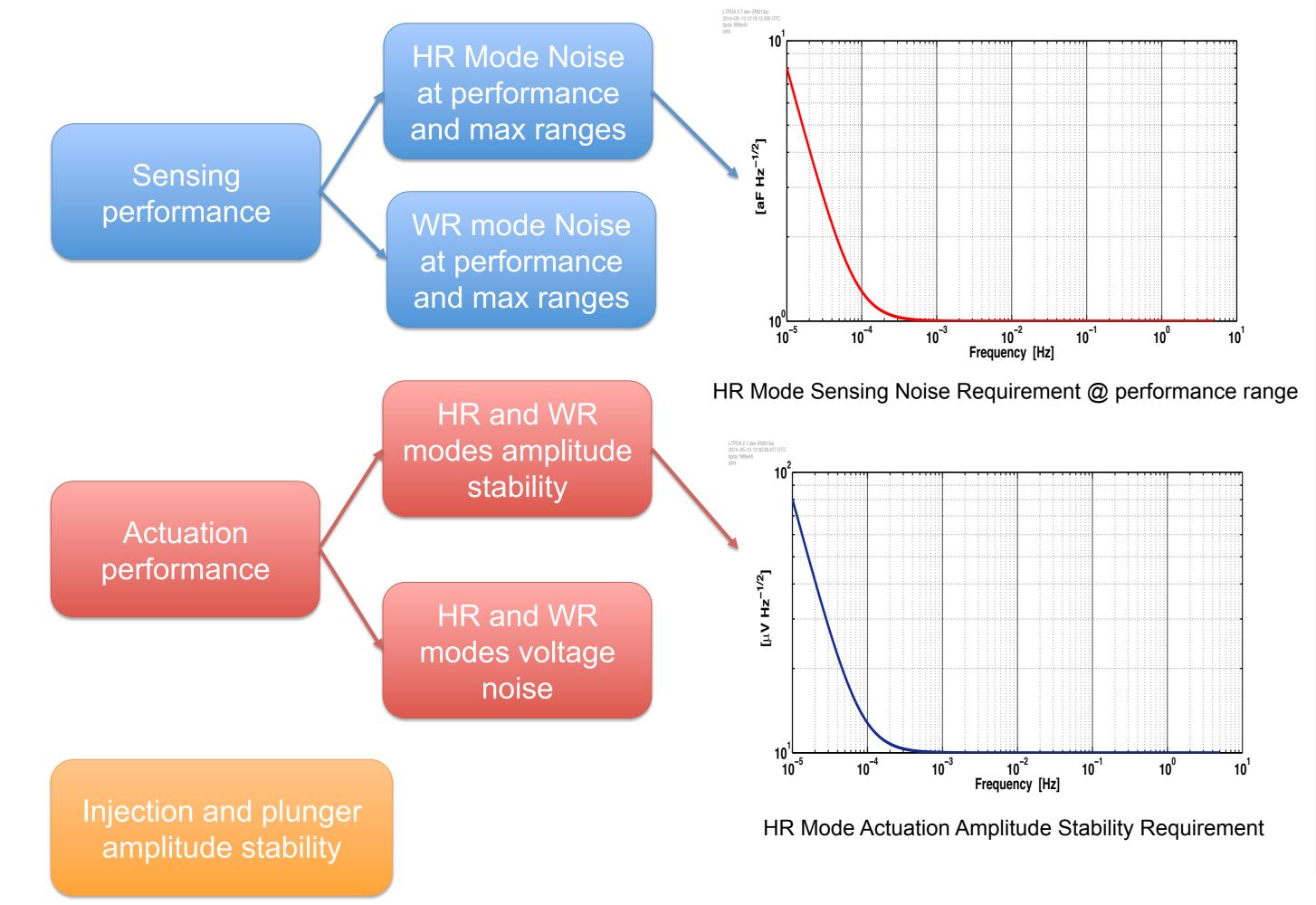
The TM is biased by 100 kHz AC bias via separate injection electrodes. To allow for high sensitivity of capacitive measurement, the transformer with high quality factor Q is operated at resonance matching the injection bias frequency, for which capacitors are added in parallel with primary windings. As the output of the preamplifier is an amplitudemodulated signal with amplitude representing the displacement, the injection bias reference (carrier) signal is also needed for the phase-sensitive detectors in the sensing circuitry chain (the demodulators).

In order to reduce the stray acceleration on TM, the force signal are provided by numerically synthesized AC waveform at low audio frequencies (60 - 270 Hz). To apply strong forces on TM in the Wide Range (WR) Mode, large AC voltages are used. The force signal for each electrode is Digital to Analog converted and amplified by a corresponding Drive Voltage Amplifier. The actuation signals are further low-pass filtered to reject higher frequencies that could interfere with the sensing circuitry.

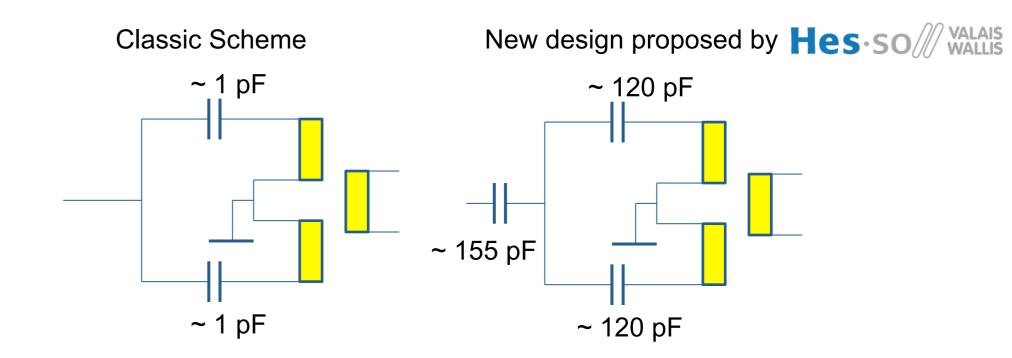
LISA GRS FEE Test Campaign – Test Setup



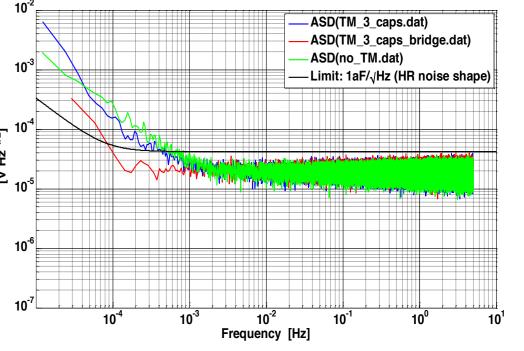
LISA GRS FEE Test Campaign



Test Mass Simulator



Instead of dual 1 pF capacitor simulator a triple capacitor design is implemented using



large capacitors

The nominal current trough the transformer is maintained and the resonance tuning $\frac{1}{2}$ capacitance is adapted to maintain the total required capacitance

The parasitic capacitances now have negligible effect on large simulator capacitors since their value is \sim 100 times larger than old design

Variation between the capacitors have ~ 100 times smaller effect on the noise

Preliminary results with a prototype

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