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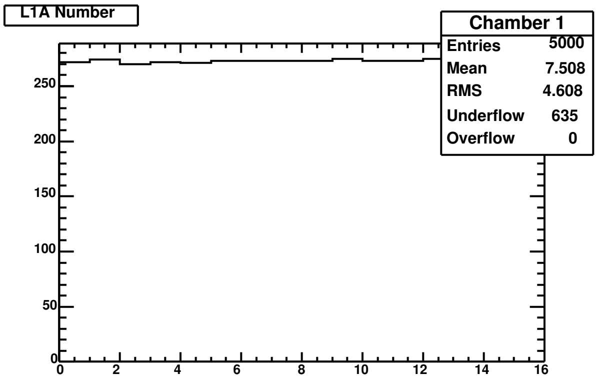

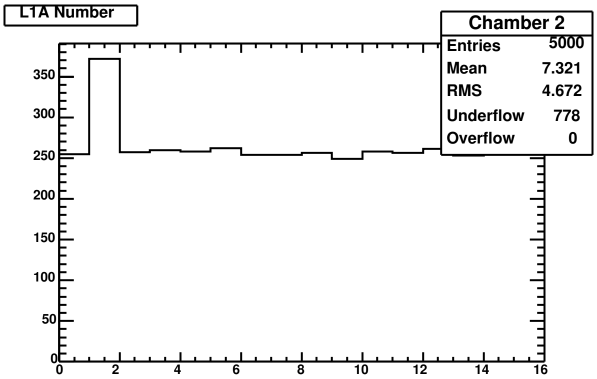

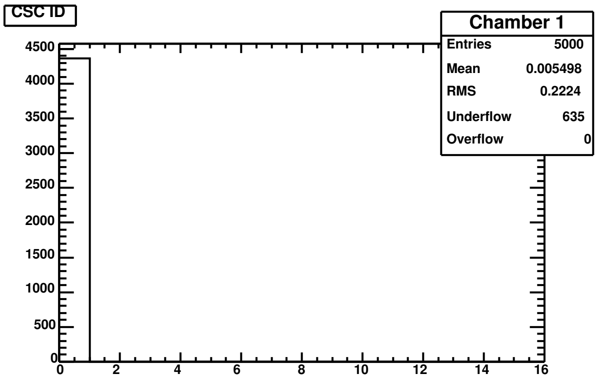

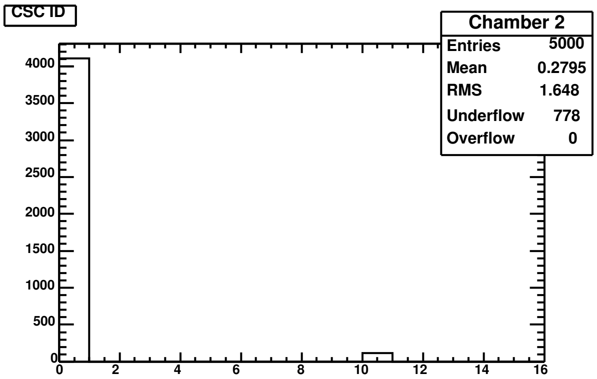

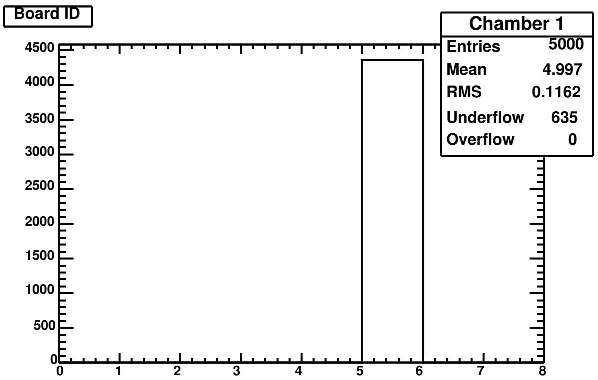

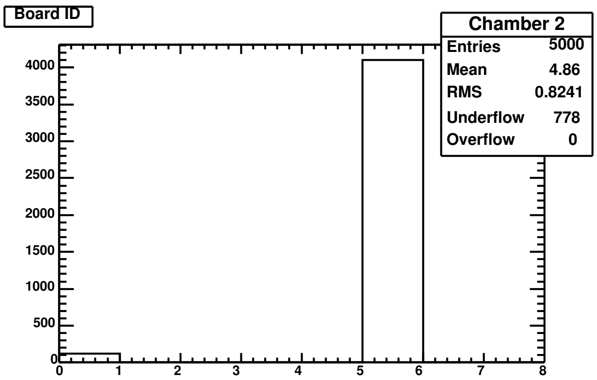

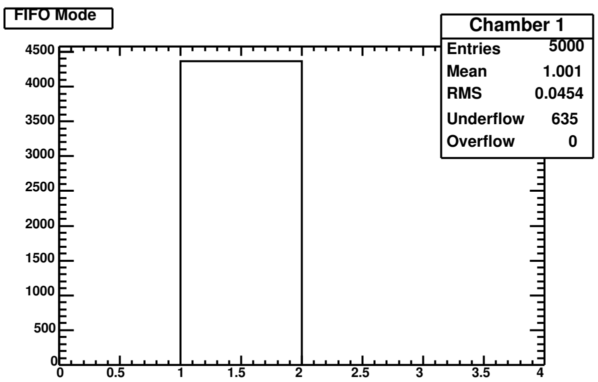

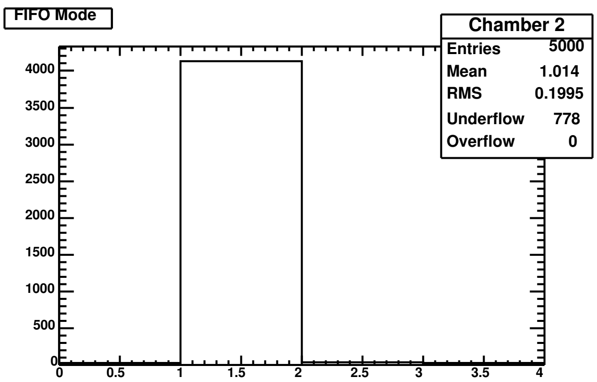

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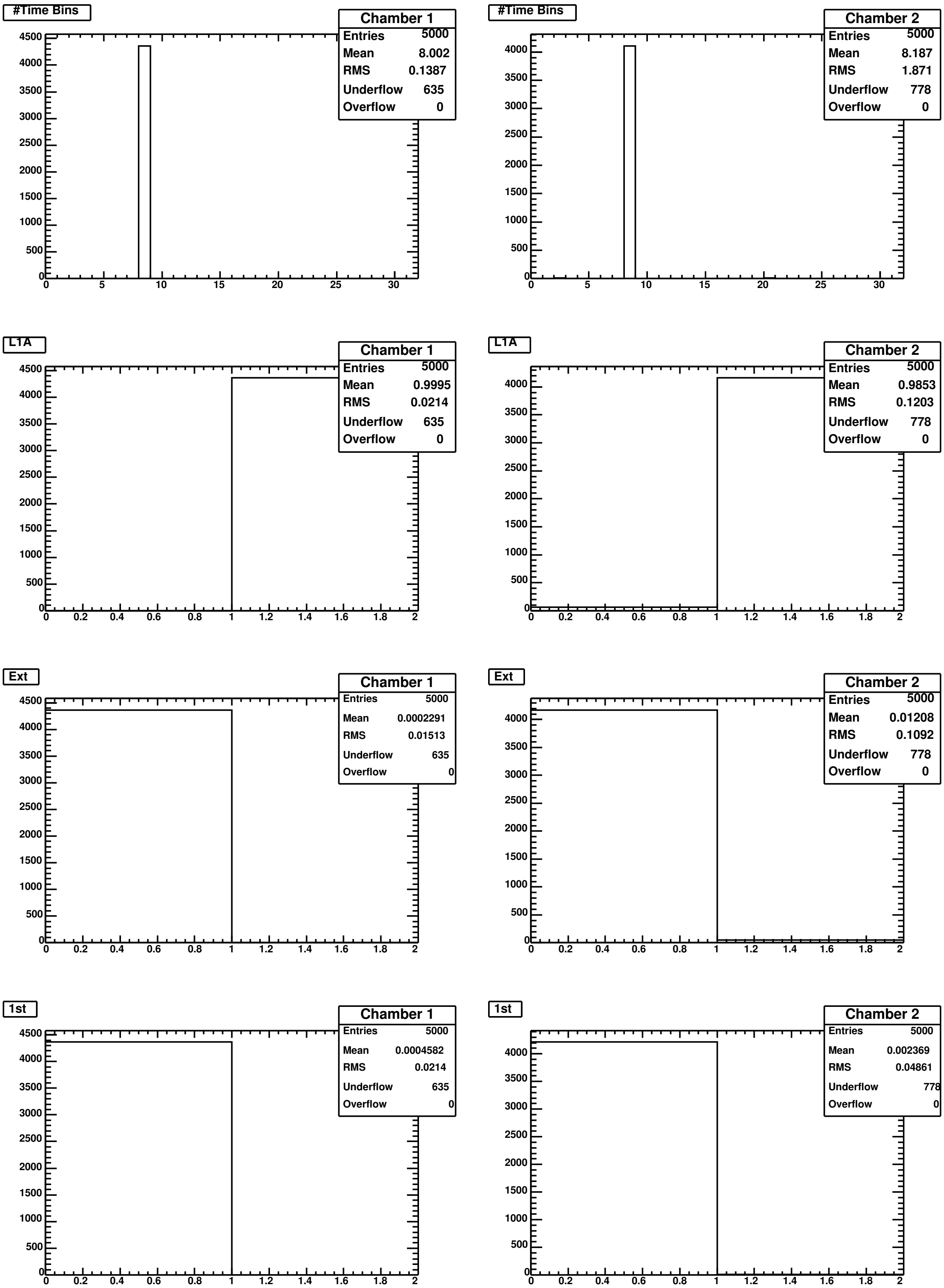

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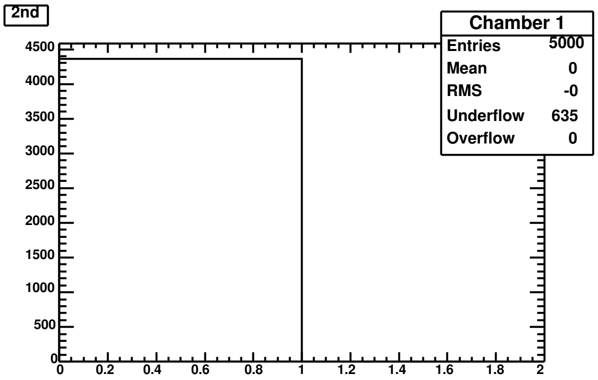

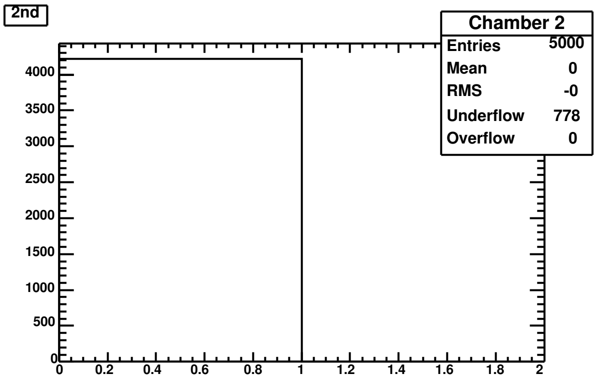

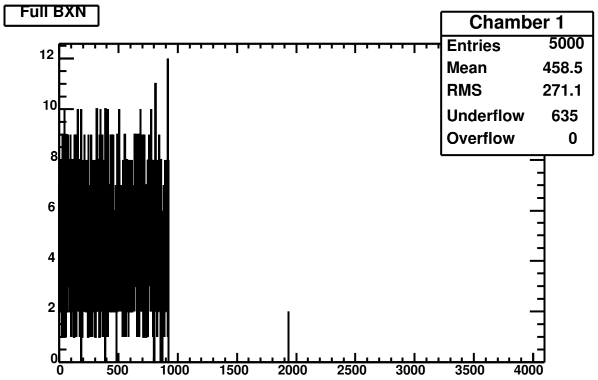

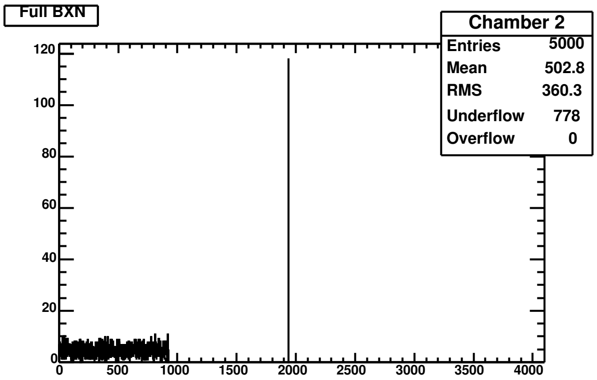

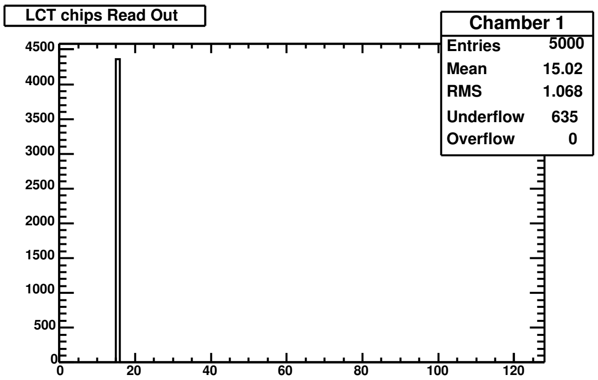

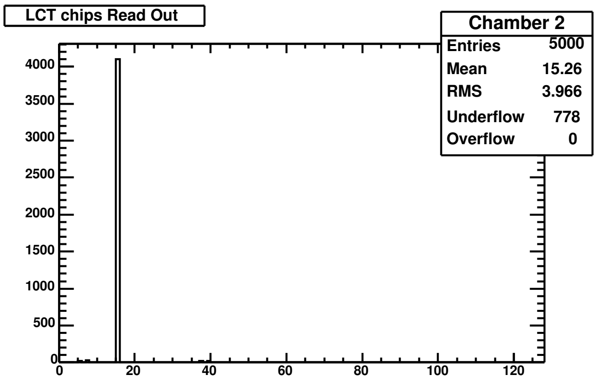

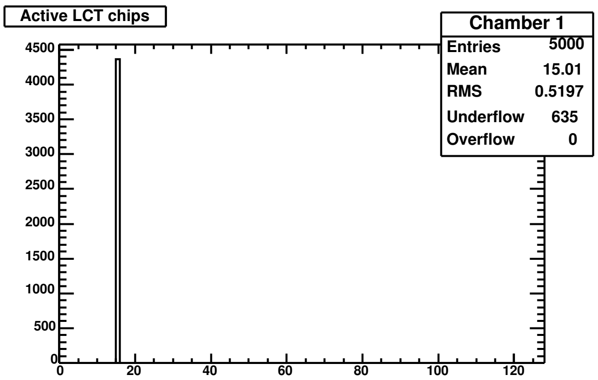

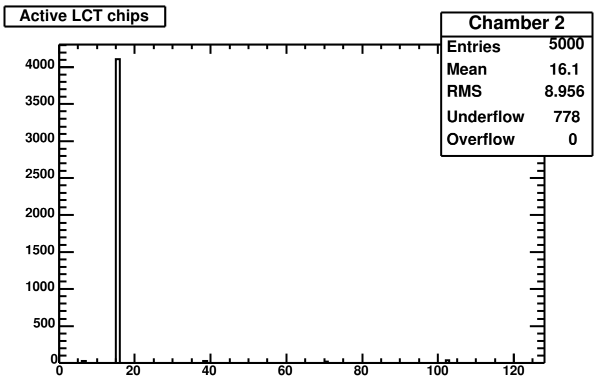

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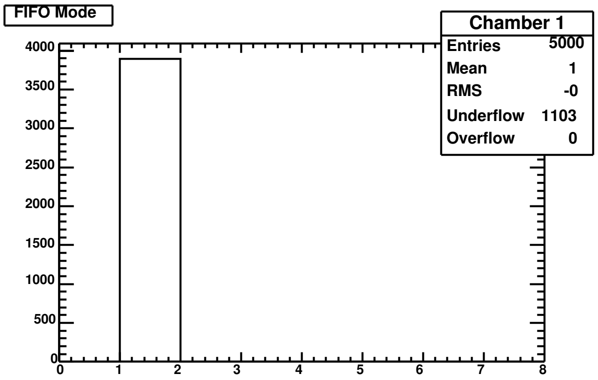

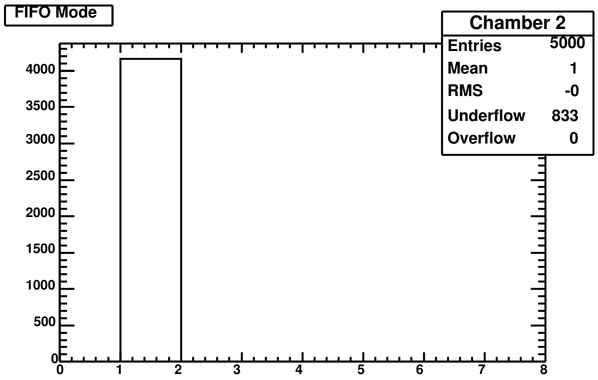

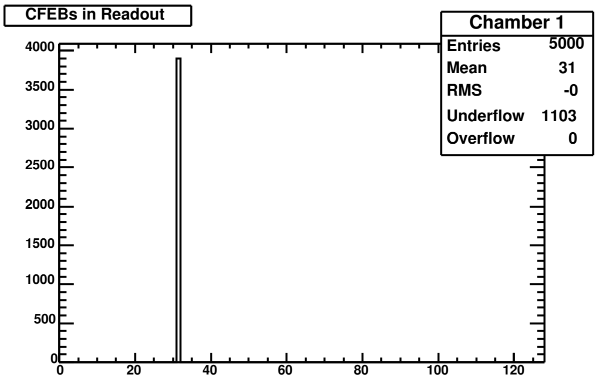

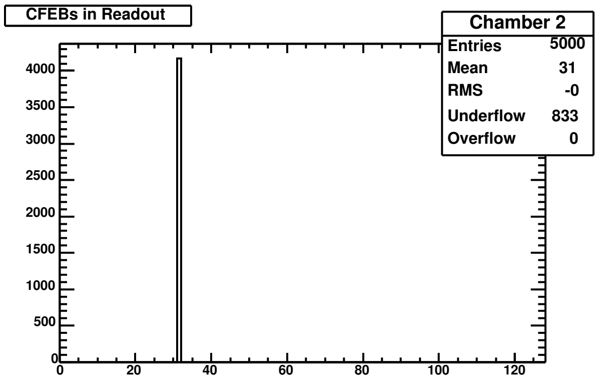

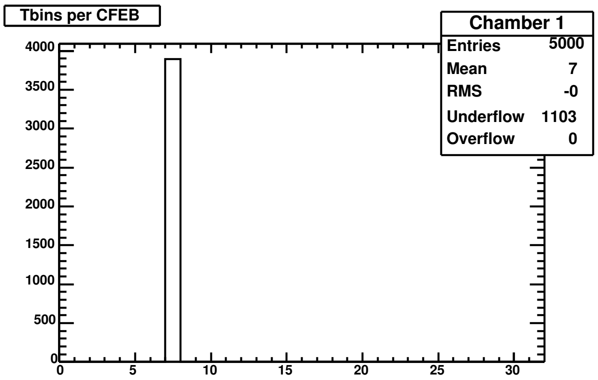

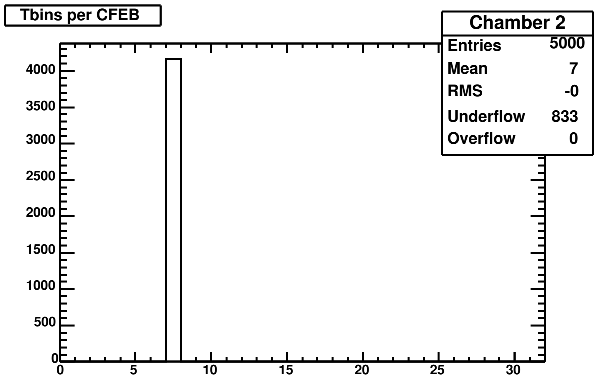

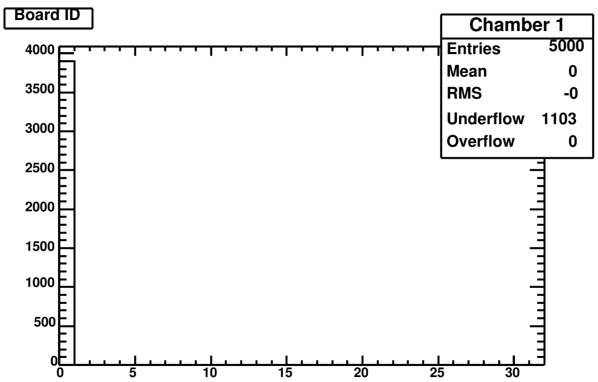

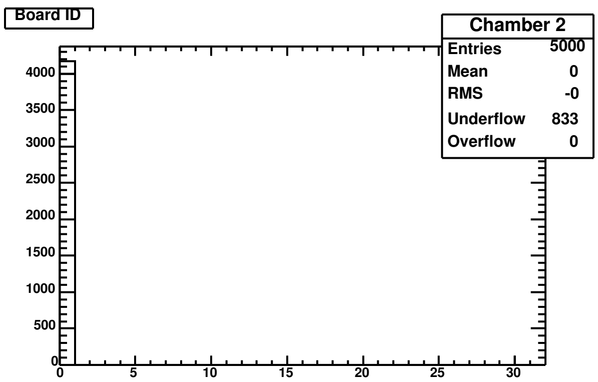

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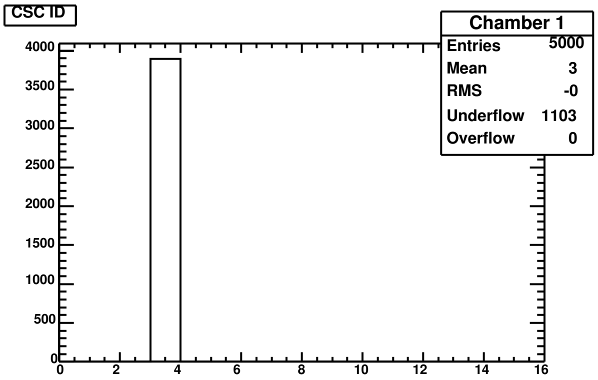

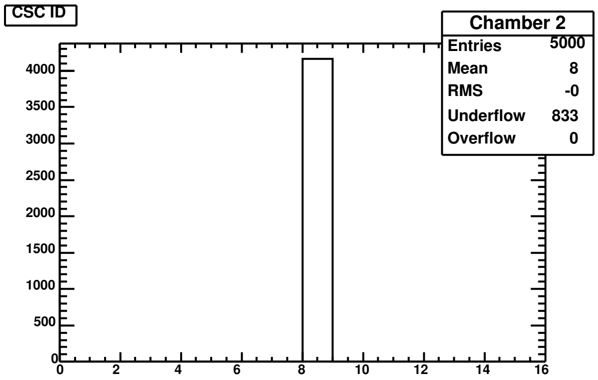

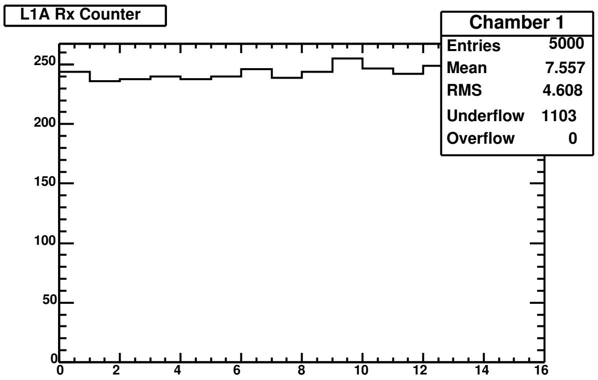

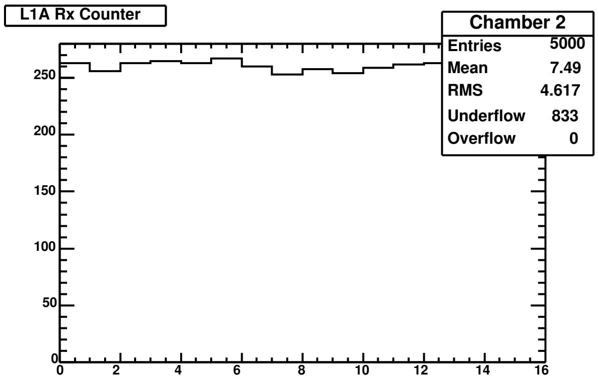

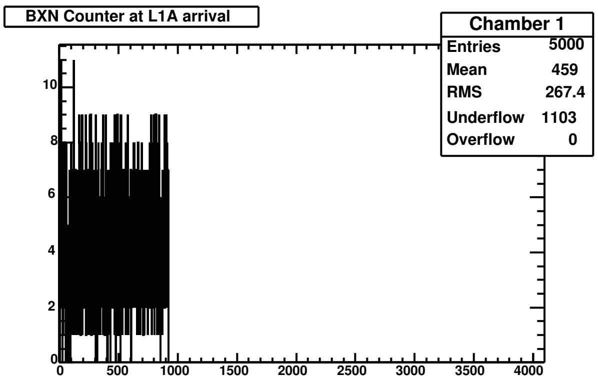

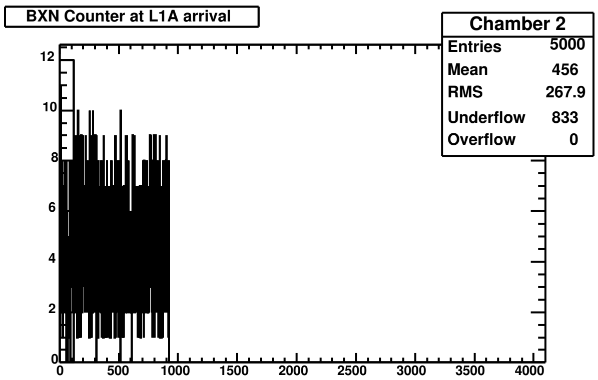

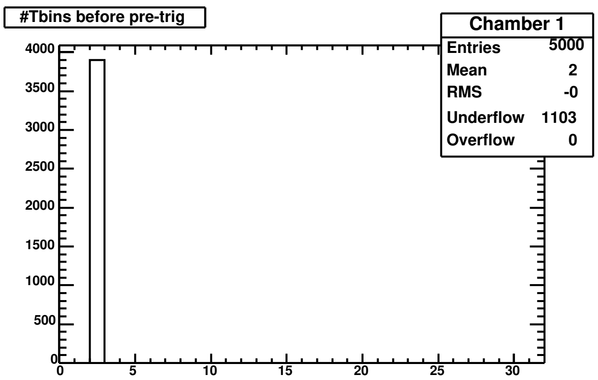

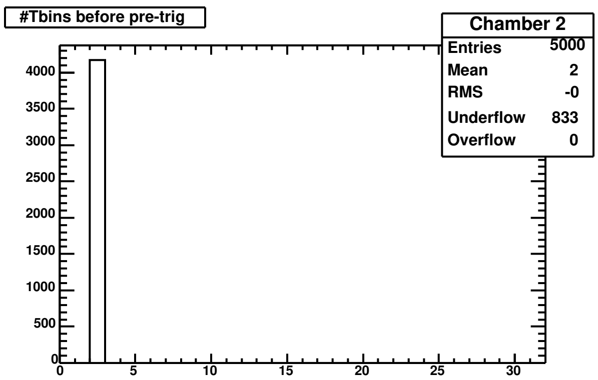

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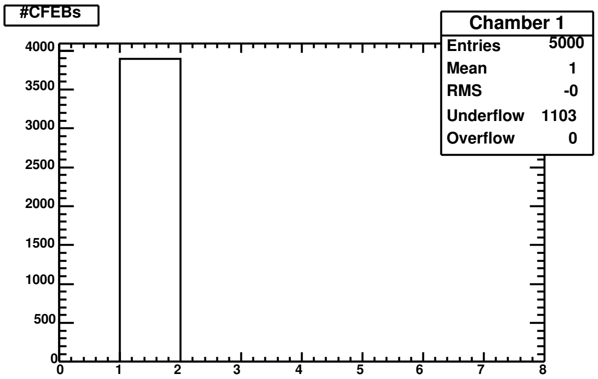
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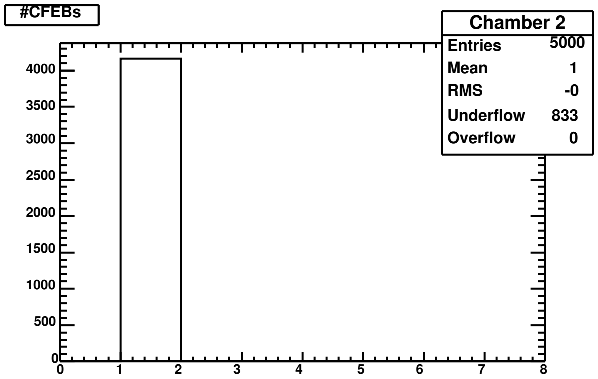
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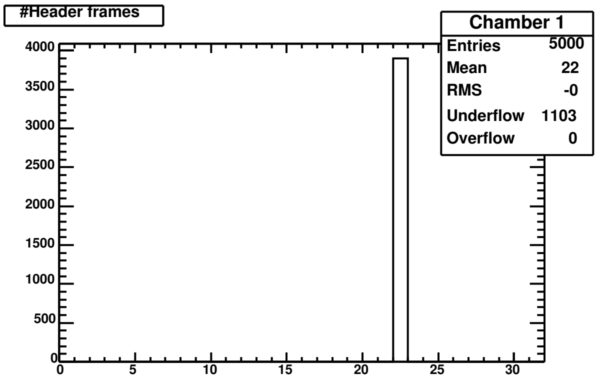
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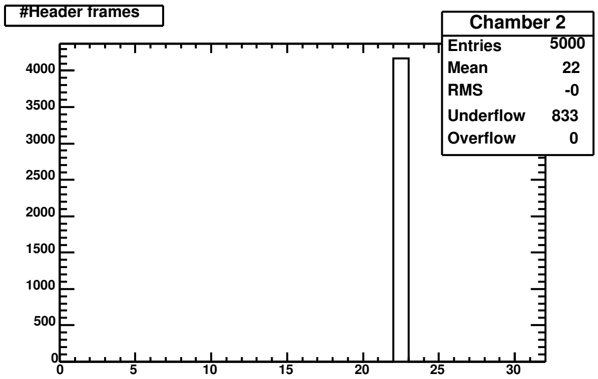
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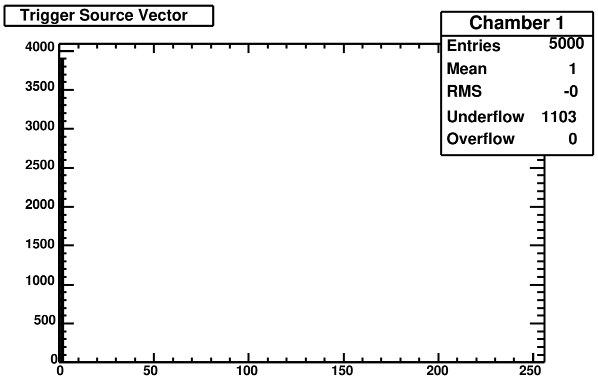
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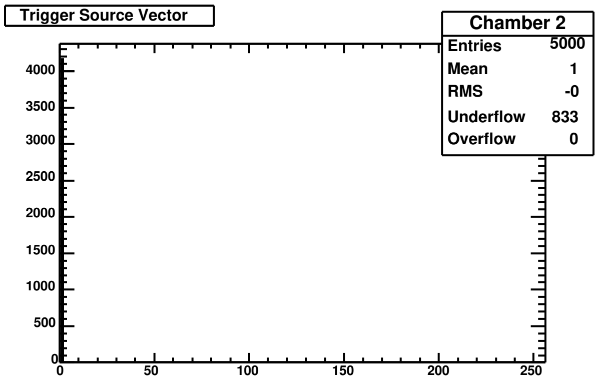
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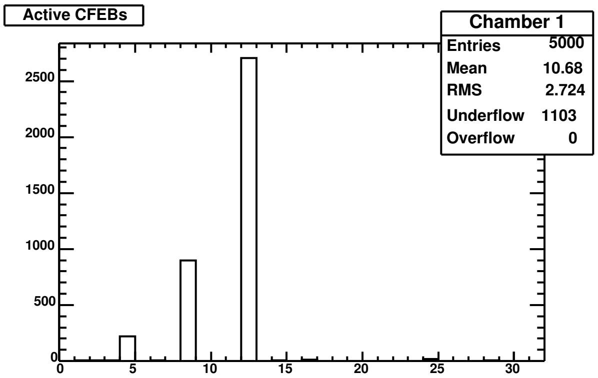
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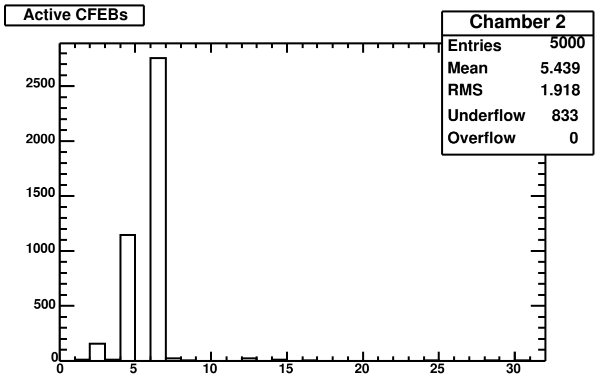
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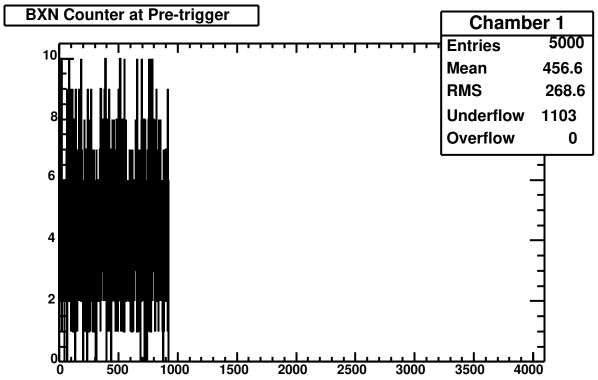

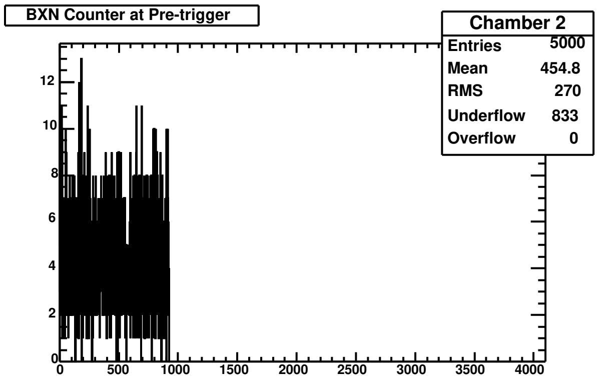

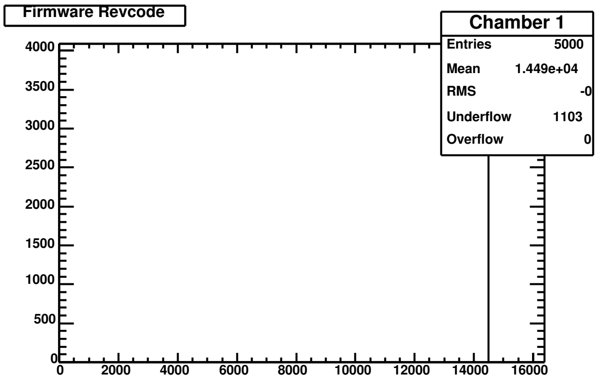

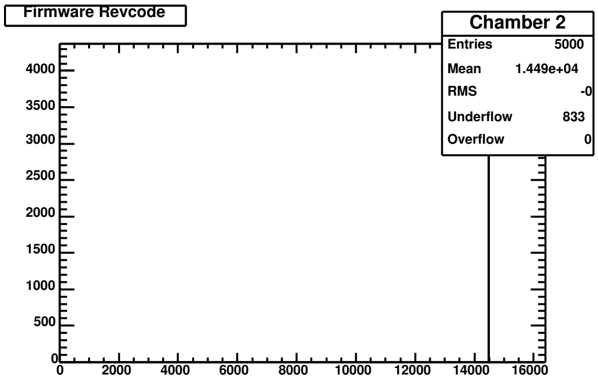

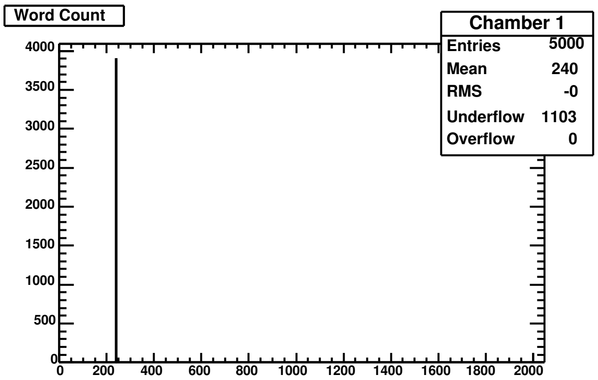

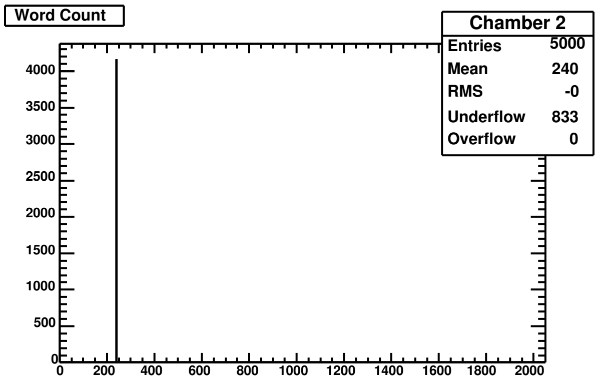

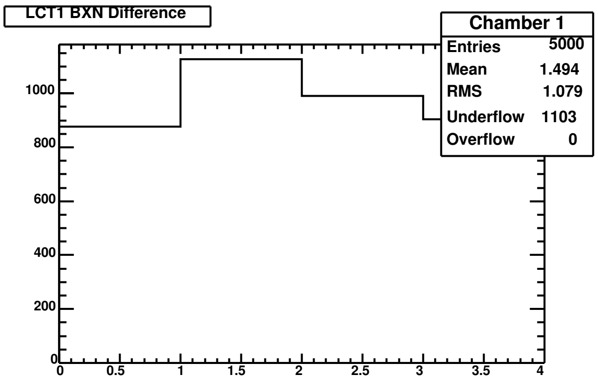

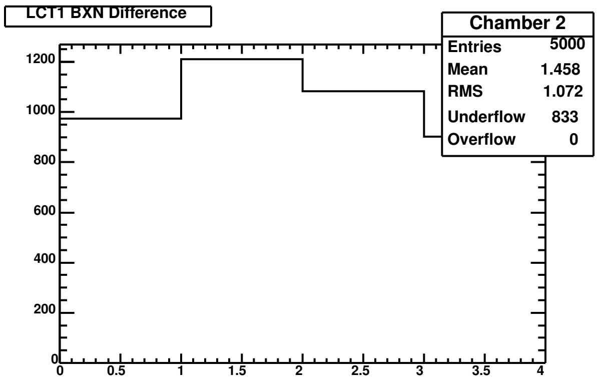

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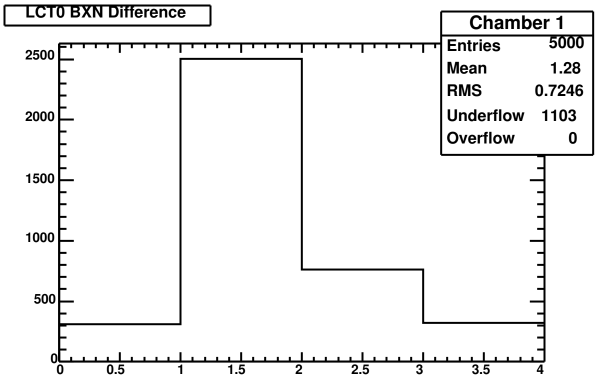

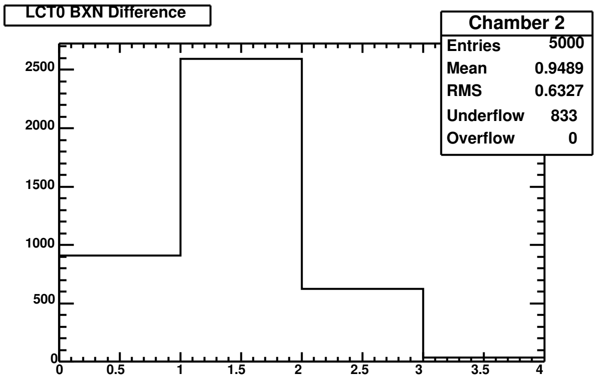

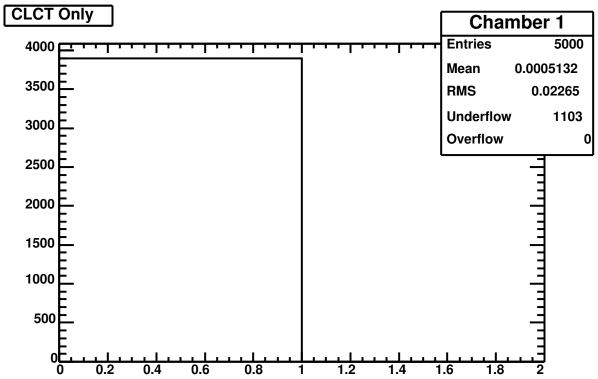

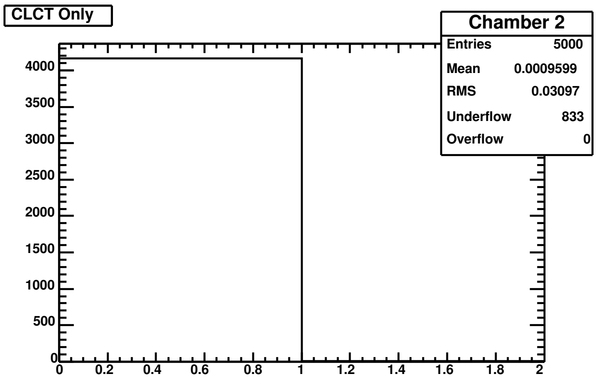

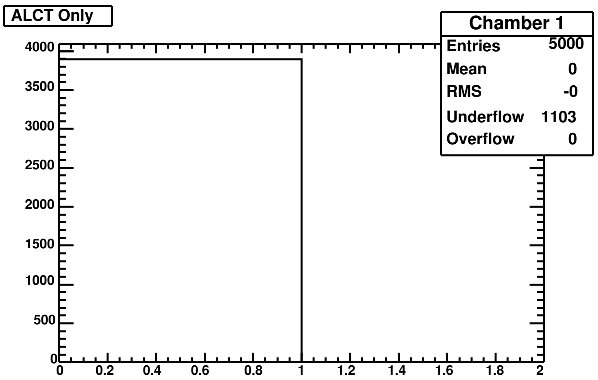

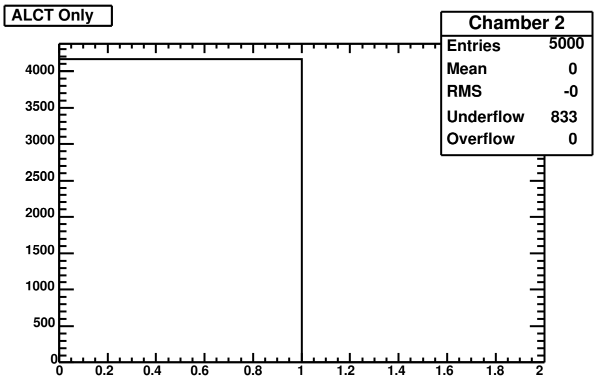

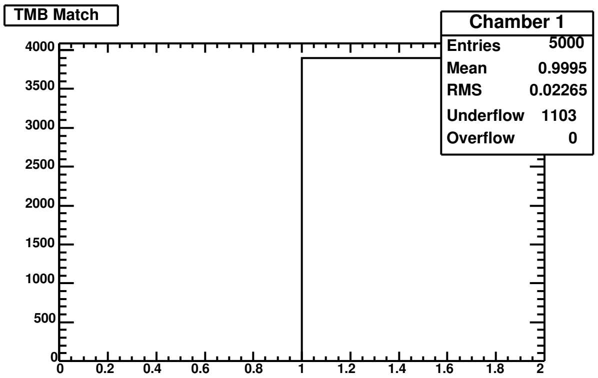

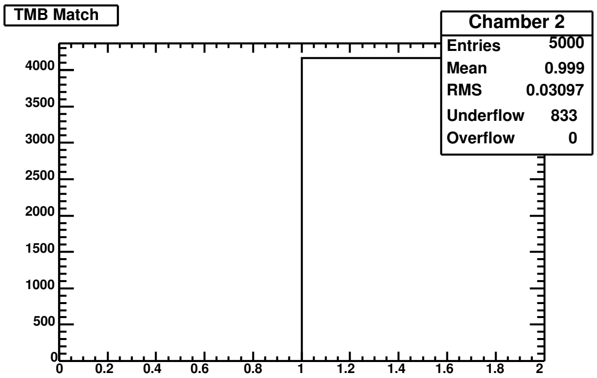

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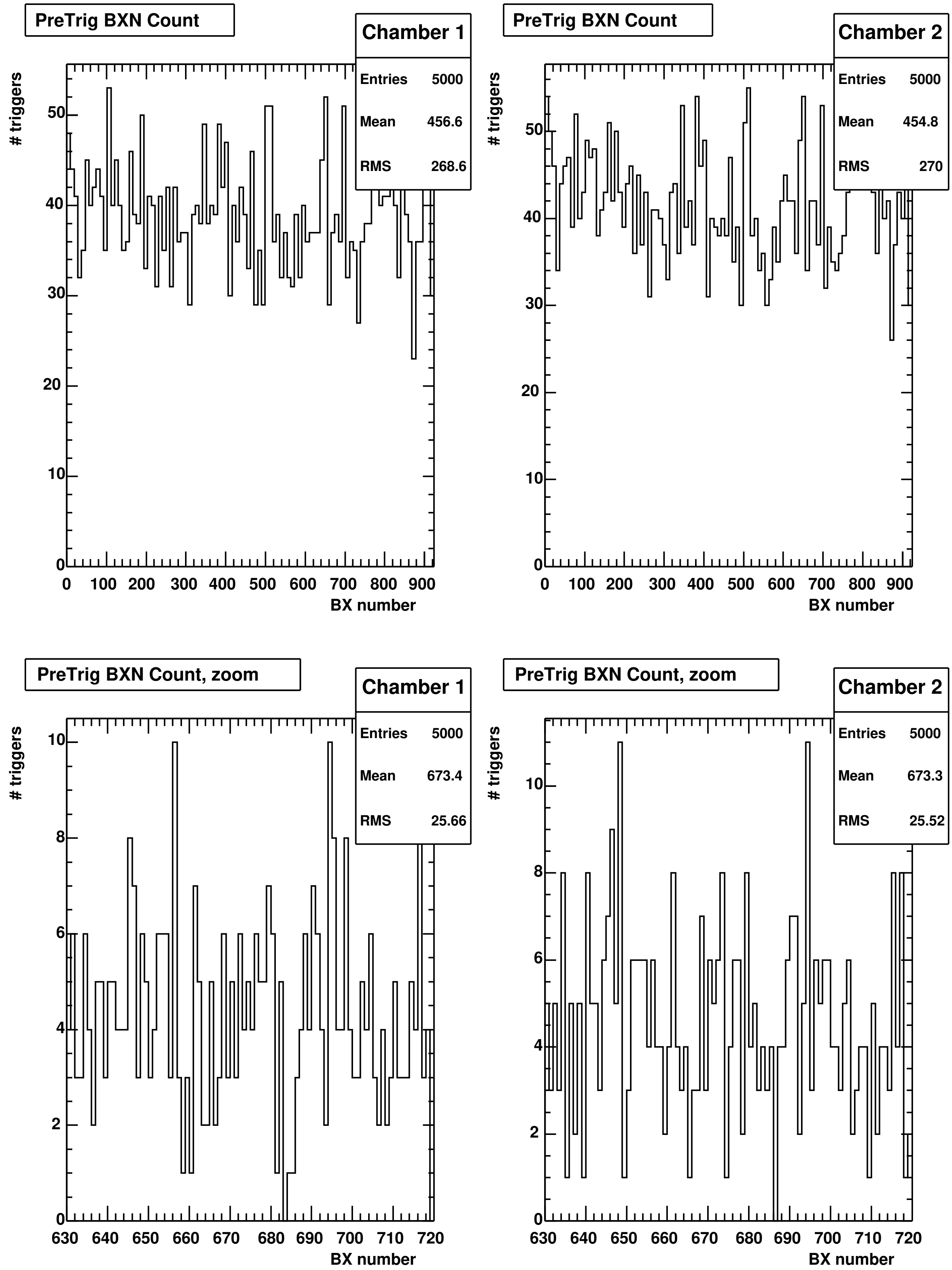
BXN Counter at Pre-trigger



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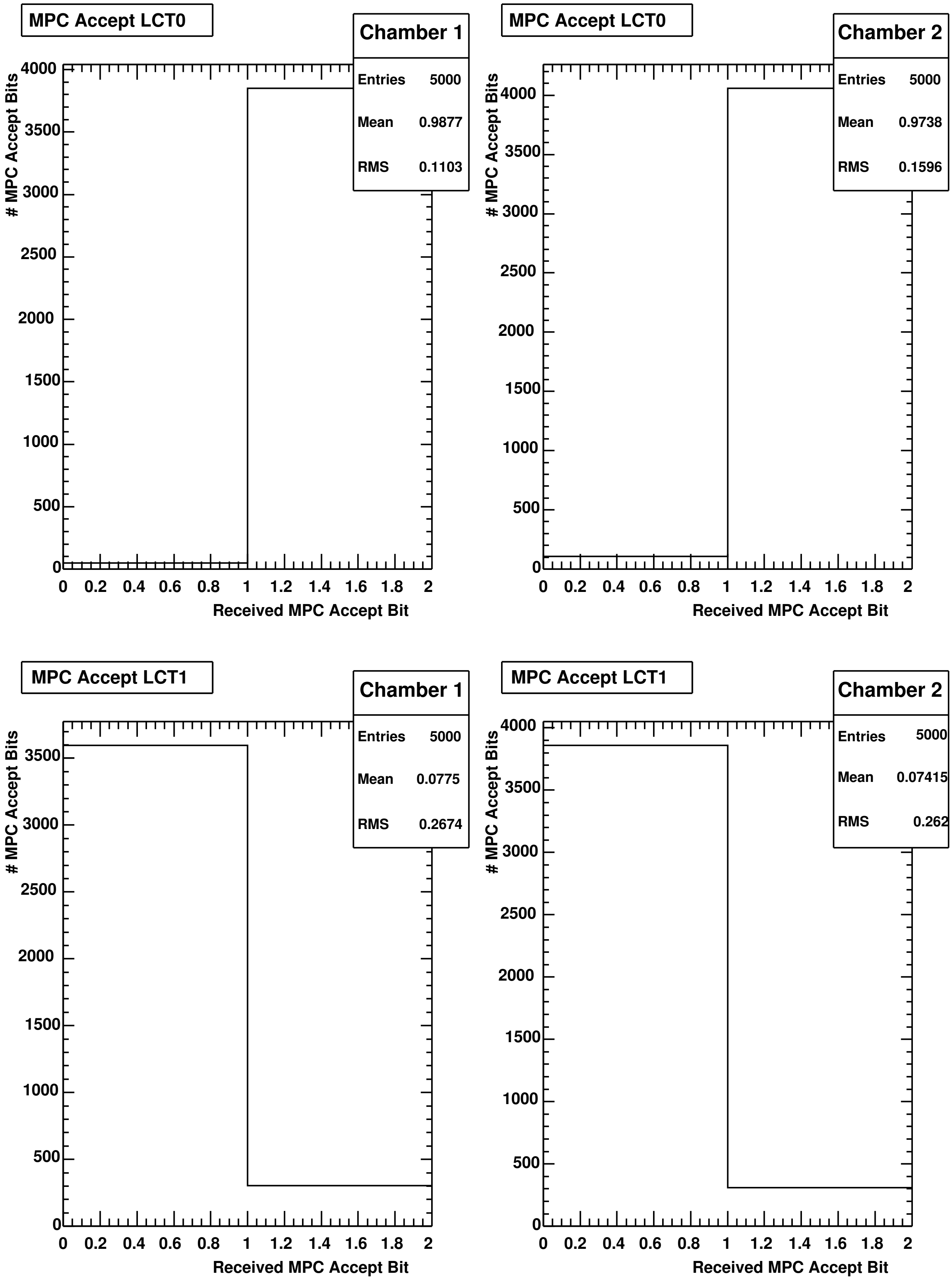
MPC Accept Bits



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