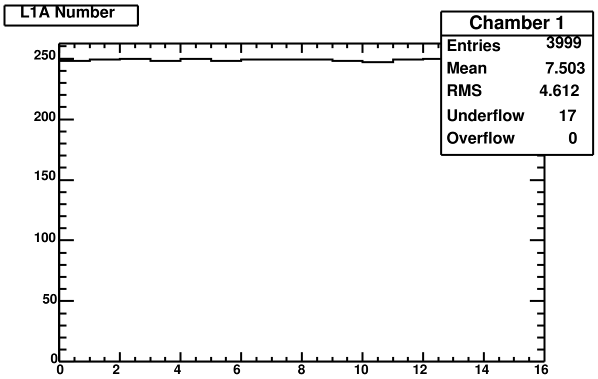
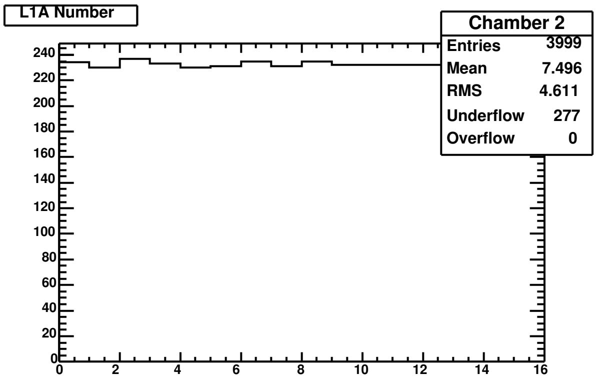
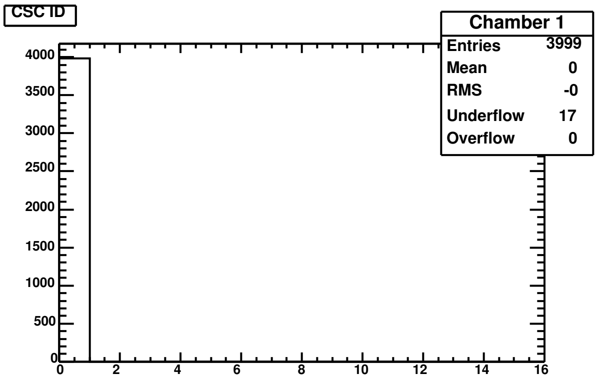
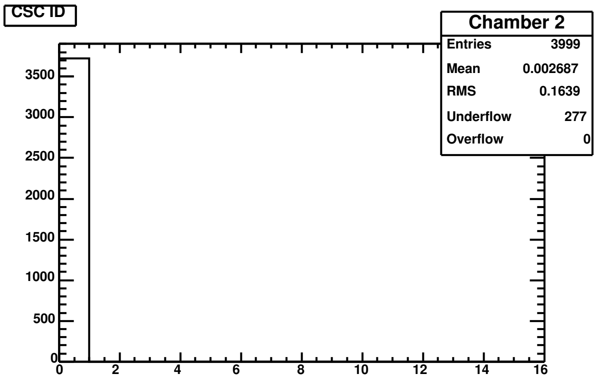
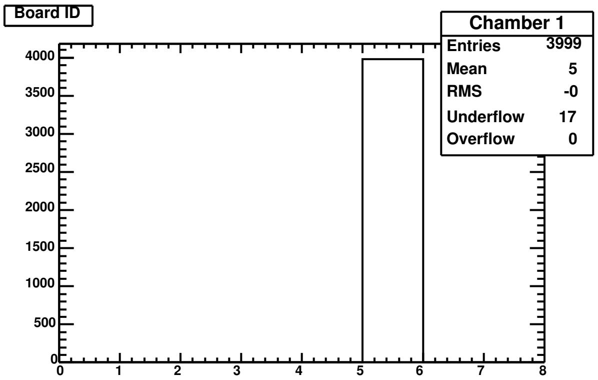
ALCT Header

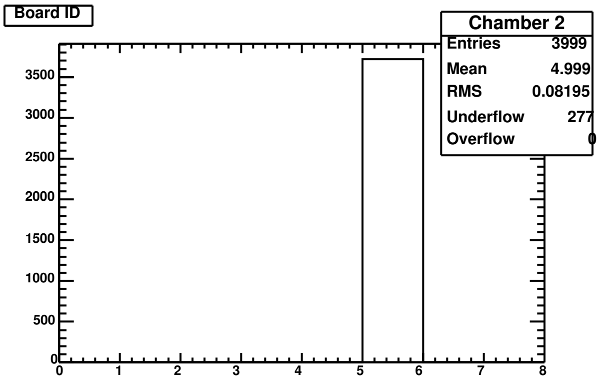


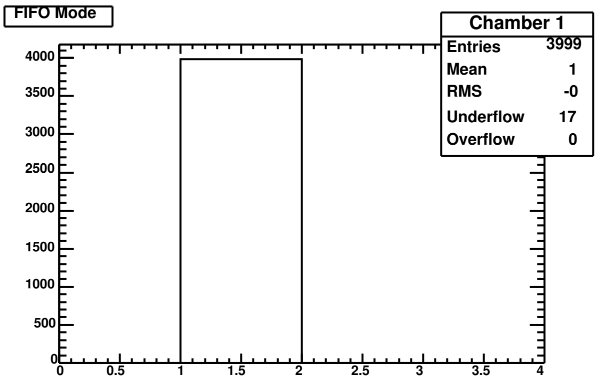


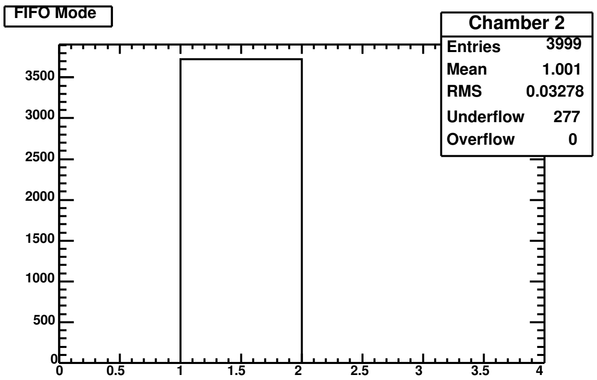












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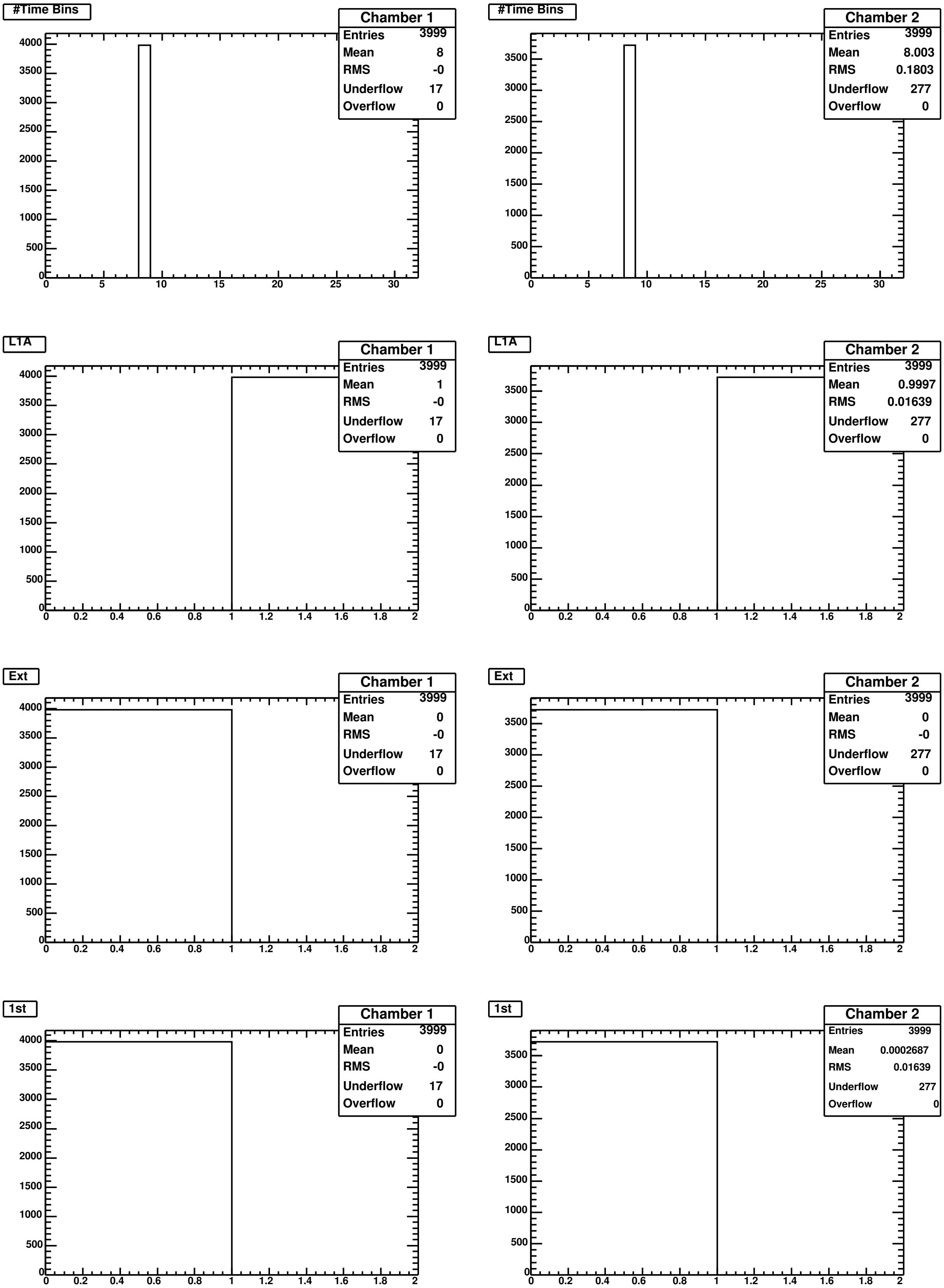
ALCT Header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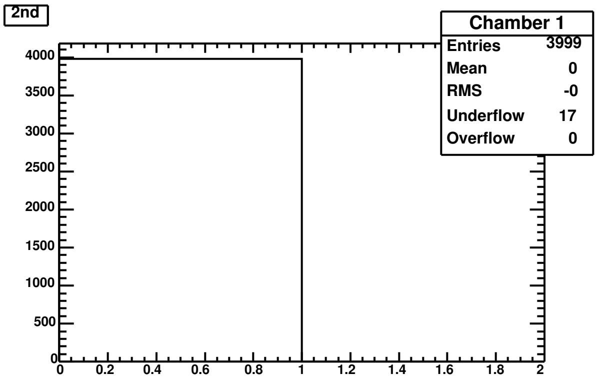
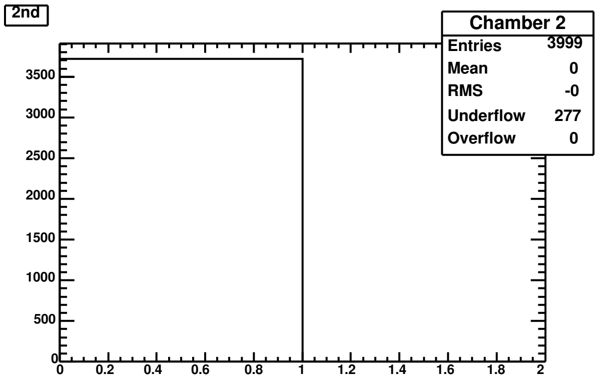
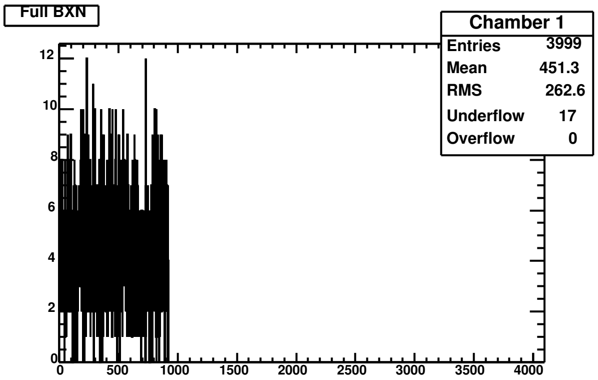
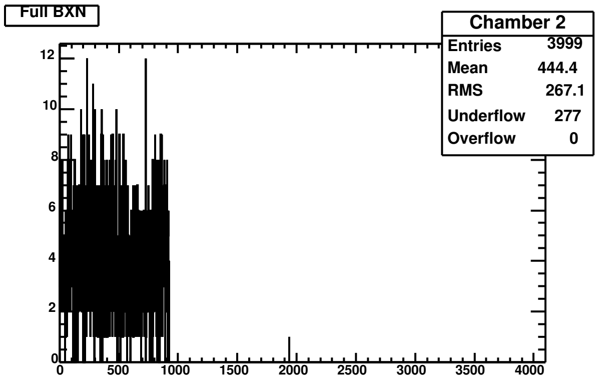
Chamber 1
Entries 3999
Mean 8
RMS -0
Underflow 17
Overflow 0
#Time Bins
Chamber 2
Entries 3999
Mean 8.003
RMS 0.1803
Underflow 277
Overflow 0
L1A
Chamber 1
Entries 3999
Mean 1
RMS -0
Underflow 17
Overflow 0
L1A
Chamber 2
Entries 3999
Mean 0.9997
RMS 0.01639
Underflow 277
Overflow 0
Ext
Chamber 1
Entries 3999
Mean 0
RMS -0
Underflow 17
Overflow 0
Ext
Chamber 2
Entries 3999
Mean 0
RMS -0
Underflow 277
Overflow 0
1st
Chamber 1
Entries 3999
Mean 0
RMS -0
Underflow 17
Overflow 0
1st
Chamber 2
Entries 3999
Mean 0.0002687
RMS 0.01639
Underflow 277
Overflow 0

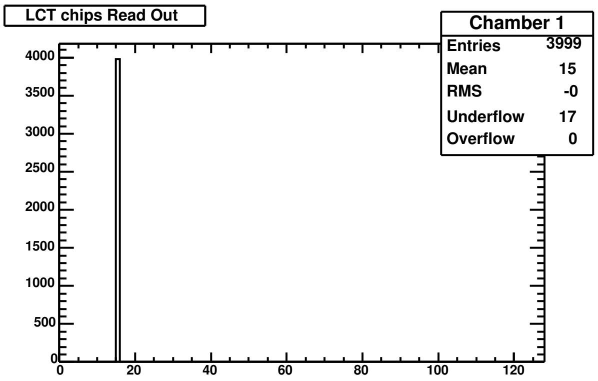
ALCT Header

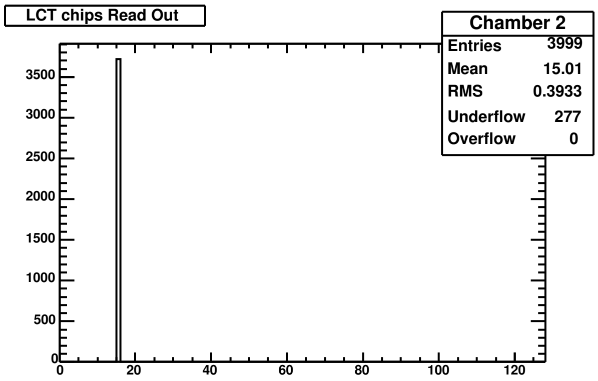

2nd
Chamber 1
Entries 3999
Mean 0
RMS -0
Underflow 17
Overflow 0

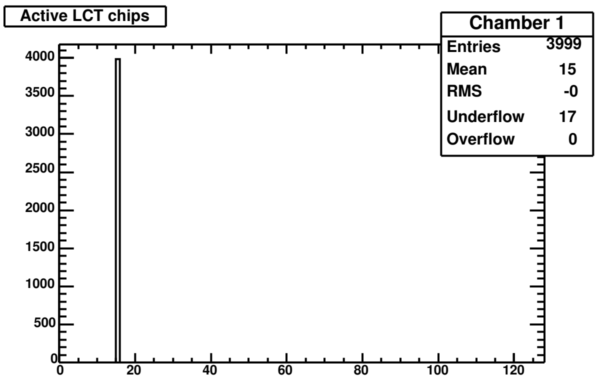

2nd
Chamber 2
Entries 3999
Mean 0
RMS -0
Underflow 277
Overflow 0

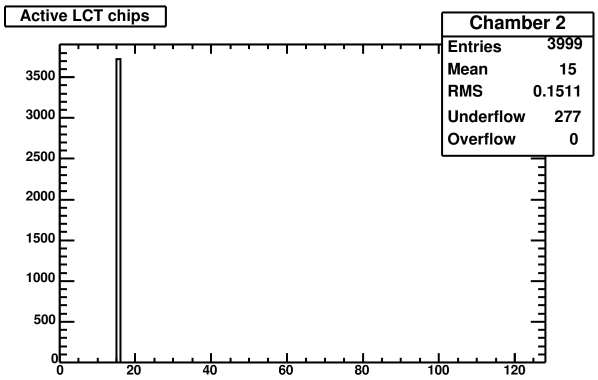

Full BXN
Chamber 1
Entries 3999
Mean 451.3
RMS 262.6
Underflow 17
Overflow 0


Full BXN
Chamber 2
Entries 3999
Mean 444.4
RMS 267.1
Underflow 277
Overflow 0

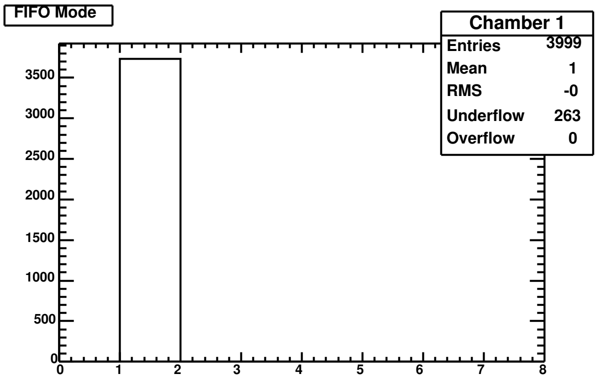

LCT chips Read Out
Chamber 1
Entries 3999
Mean 15
RMS -0
Underflow 17
Overflow 0

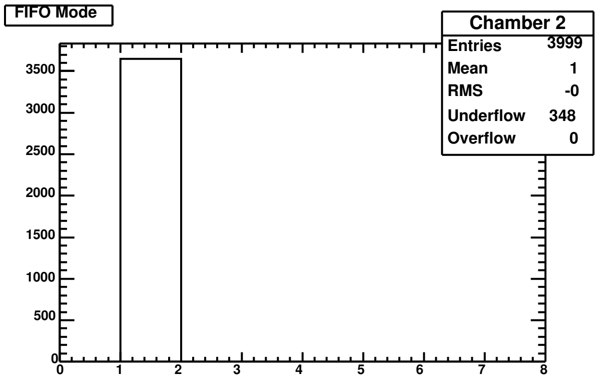

LCT chips Read Out
Chamber 2
Entries 3999
Mean 15.01
RMS 0.3933
Underflow 277
Overflow 0

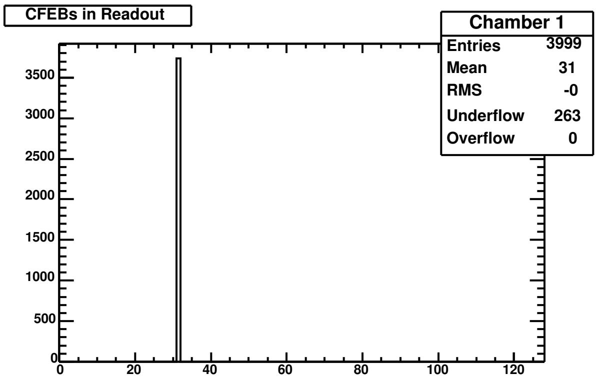

Active LCT chips
Chamber 1
Entries 3999
Mean 15
RMS -0
Underflow 17
Overflow 0

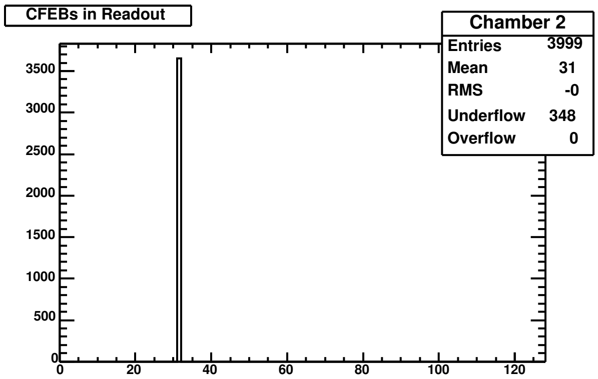

Active LCT chips
Chamber 2
Entries 3999
Mean 15
RMS 0.1511
Underflow 277
Overflow 0

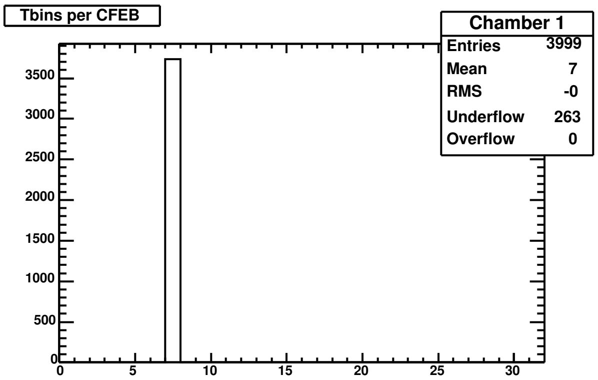
TMB Header

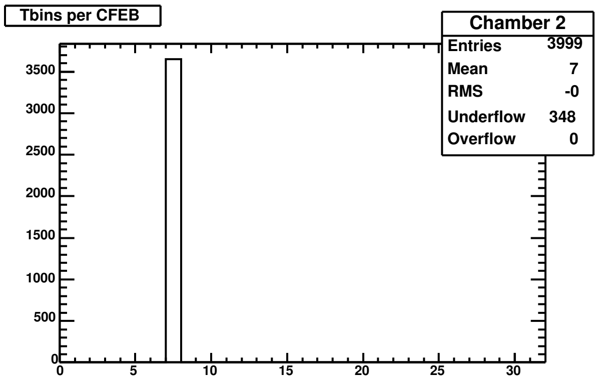


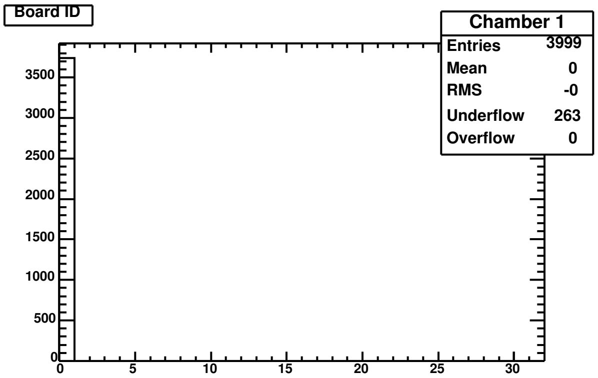


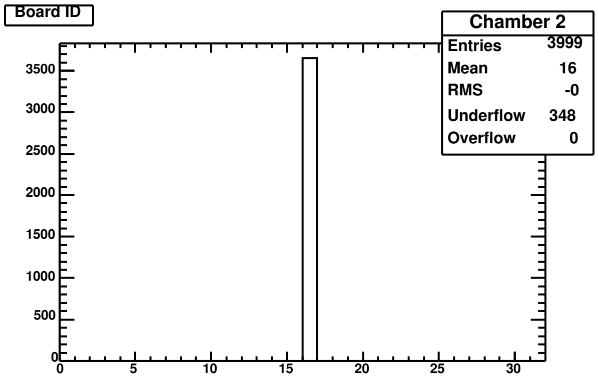












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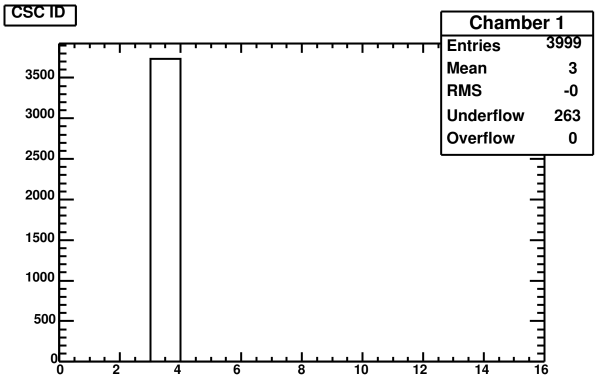

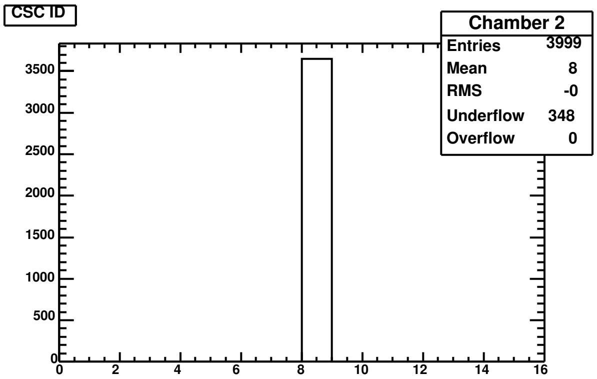

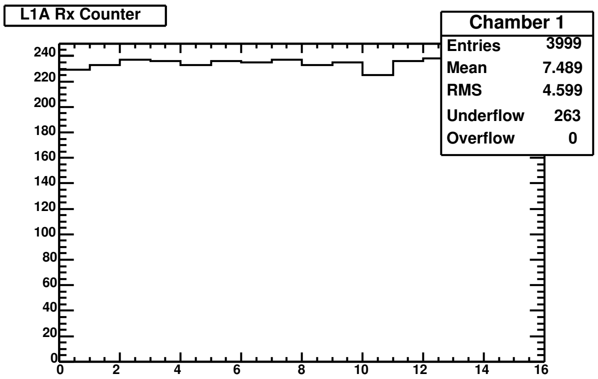

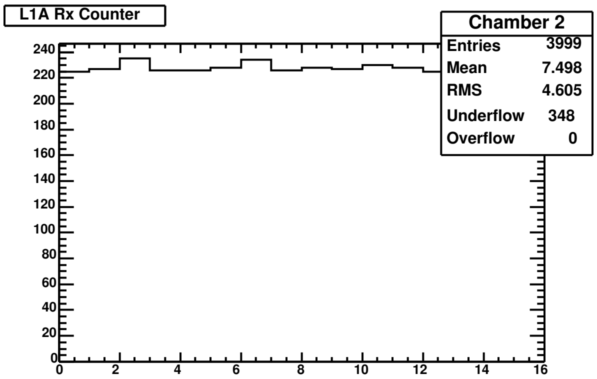

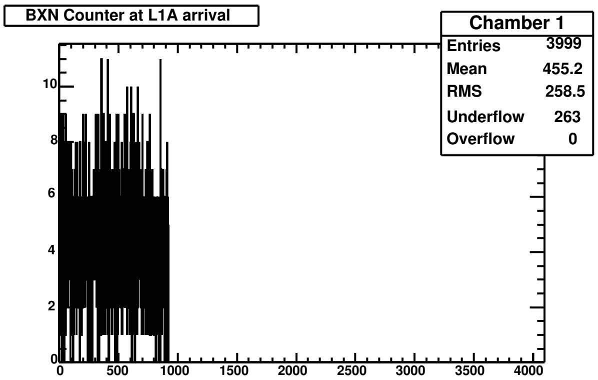

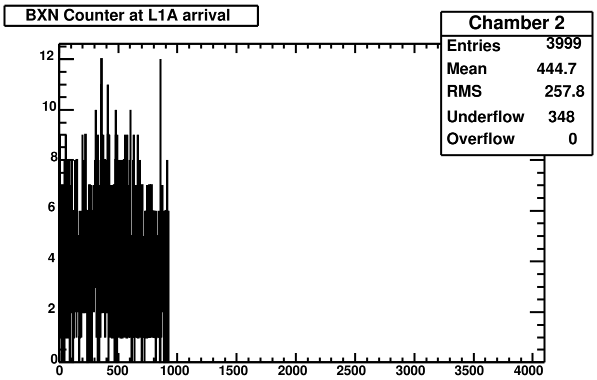

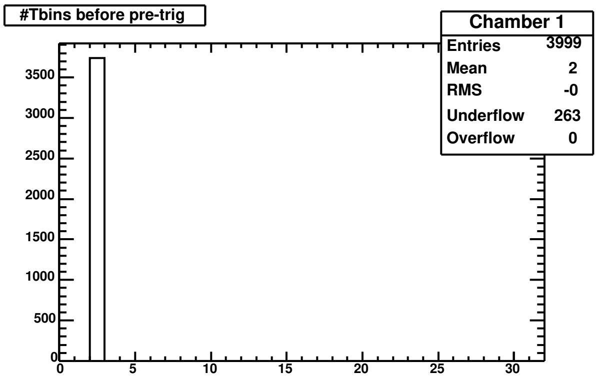

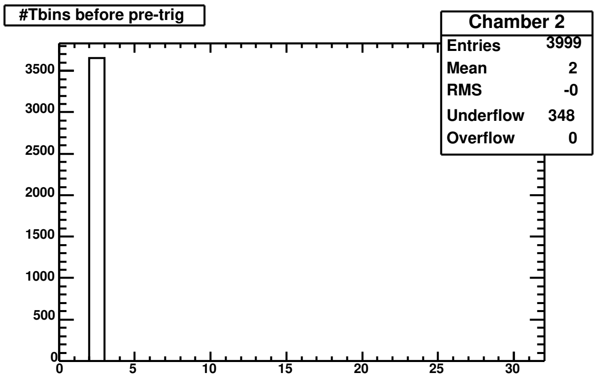

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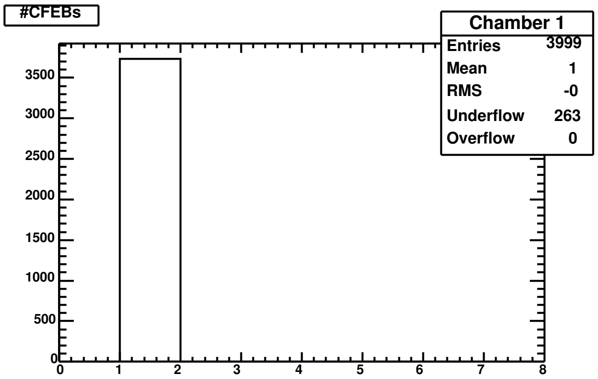
TMB Header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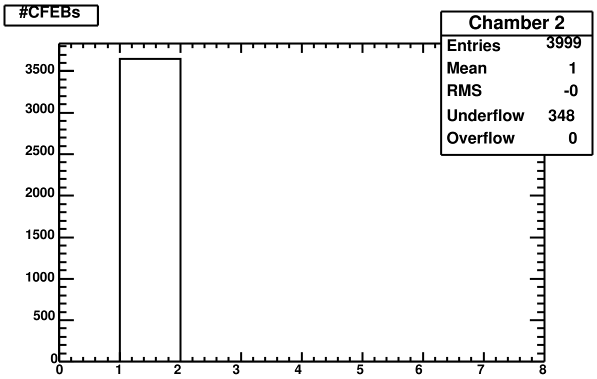
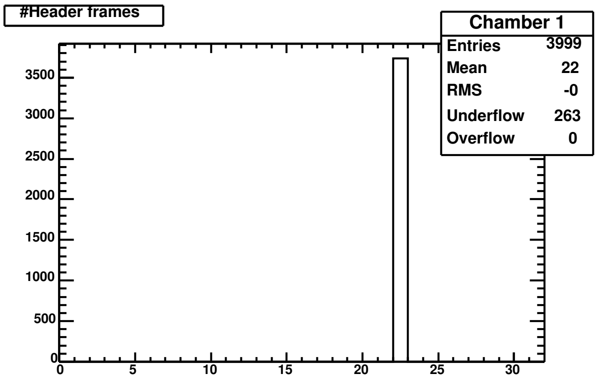
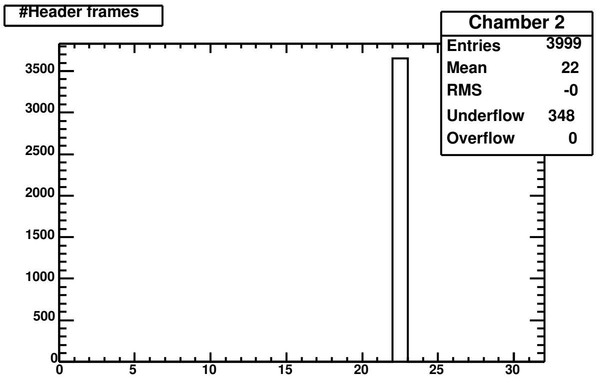
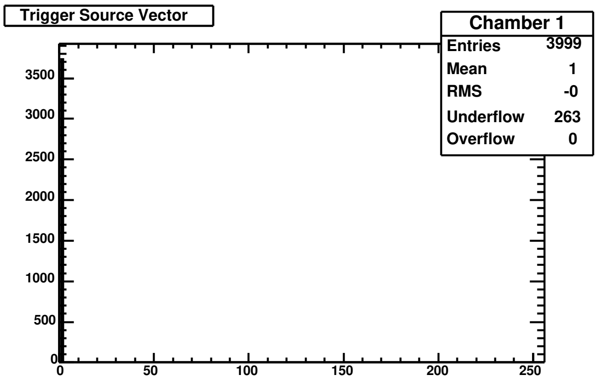
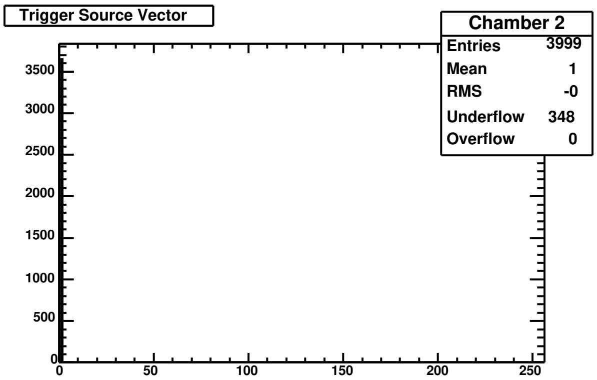
Chamber 1
Entries 3999
Mean 1
RMS -0
Underflow 263
Overflow 0

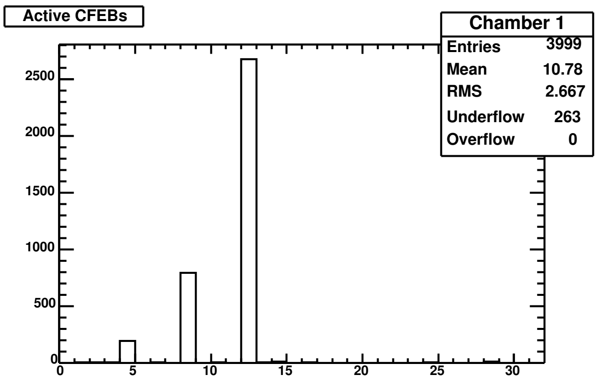

#CFEBs
Chamber 2
Entries 3999
Mean 1
RMS -0
Underflow 348
Overflow 0

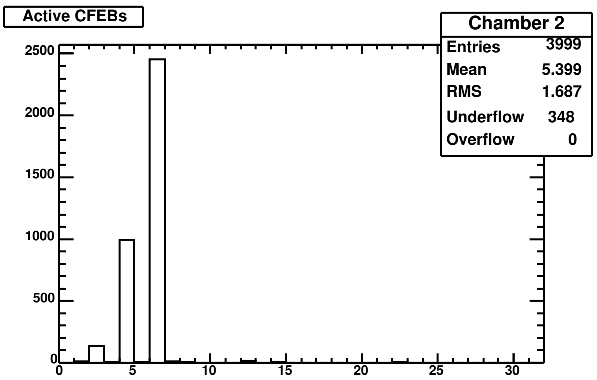

#Header frames
Chamber 1
Entries 3999
Mean 22
RMS -0
Underflow 263
Overflow 0


#Header frames
Chamber 2
Entries 3999
Mean 22
RMS -0
Underflow 348
Overflow 0

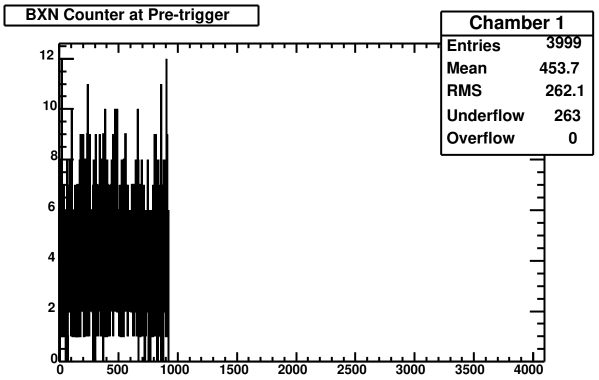

Trigger Source Vector
Chamber 1
Entries 3999
Mean 1
RMS -0
Underflow 263
Overflow 0

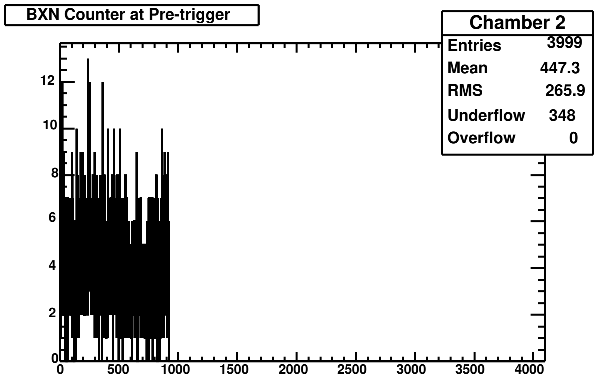

Trigger Source Vector
Chamber 2
Entries 3999
Mean 1
RMS -0
Underflow 348
Overflow 0

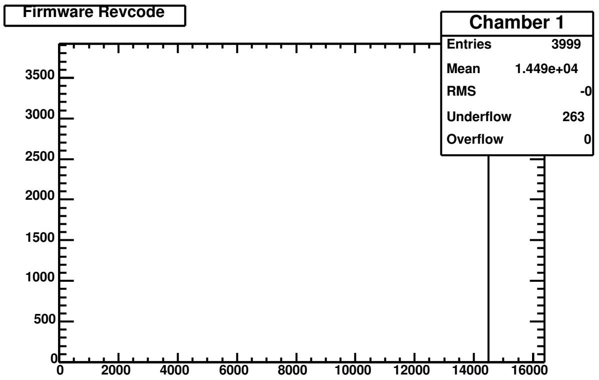

Active CFEBs
Chamber 1
Entries 3999
Mean 10.78
RMS 2.667
Underflow 263
Overflow 0

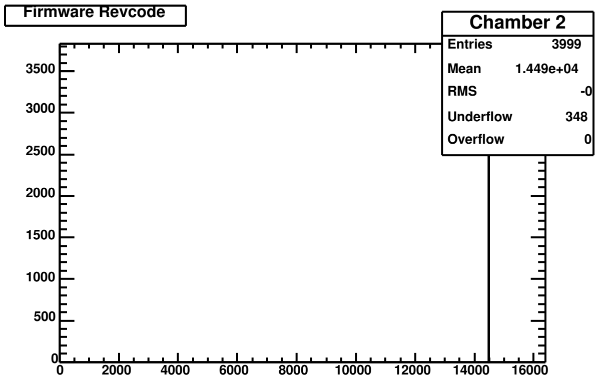

Active CFEBs
Chamber 2
Entries 3999
Mean 5.399
RMS 1.687
Underflow 348
Overflow 0

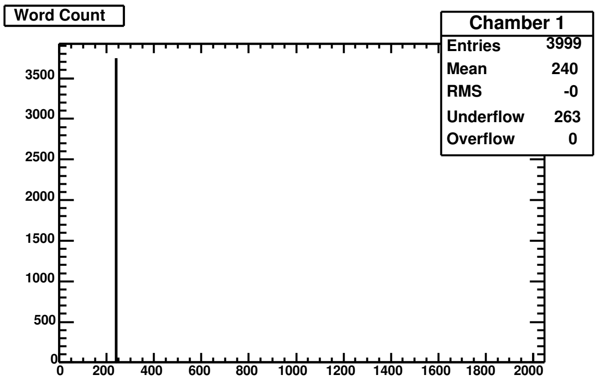
TMB Header

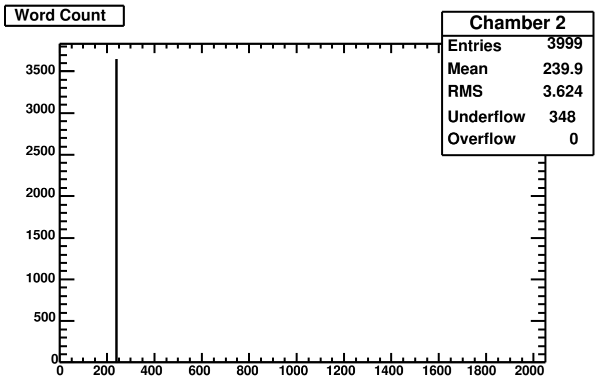


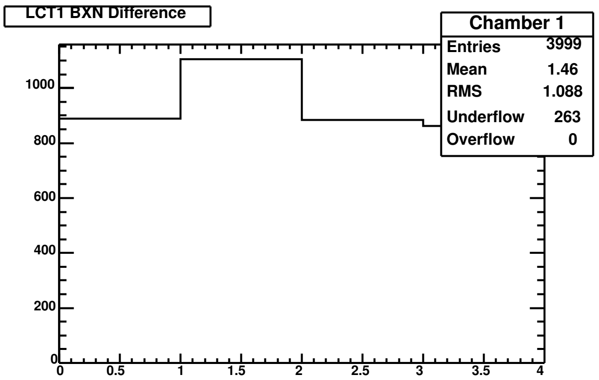


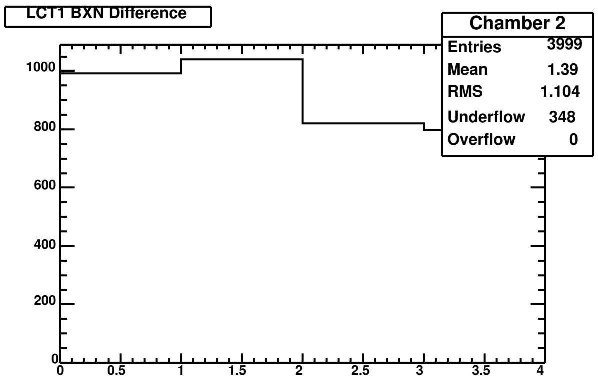












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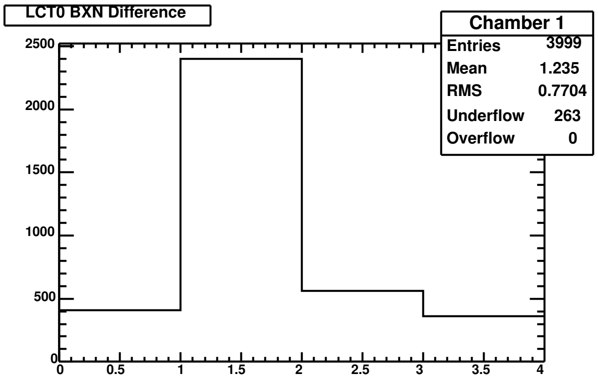

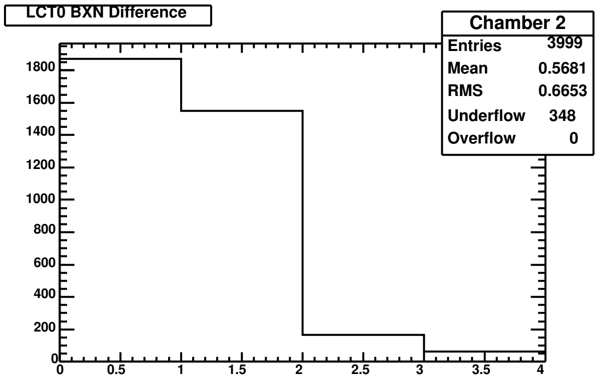

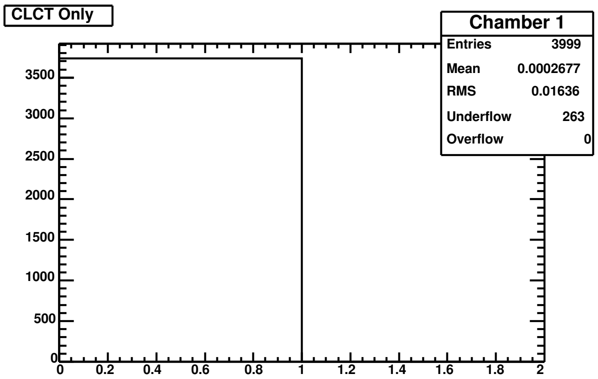

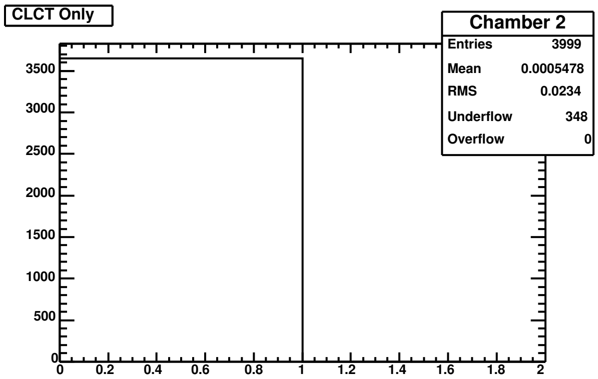

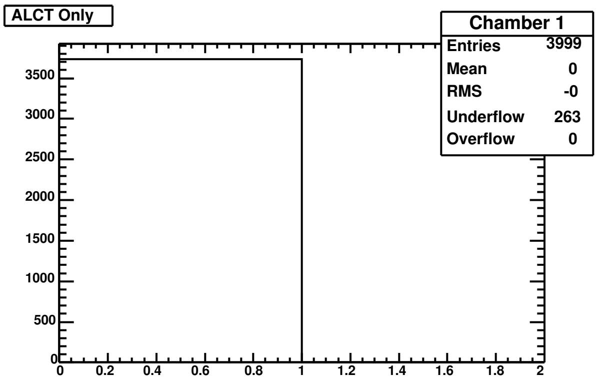

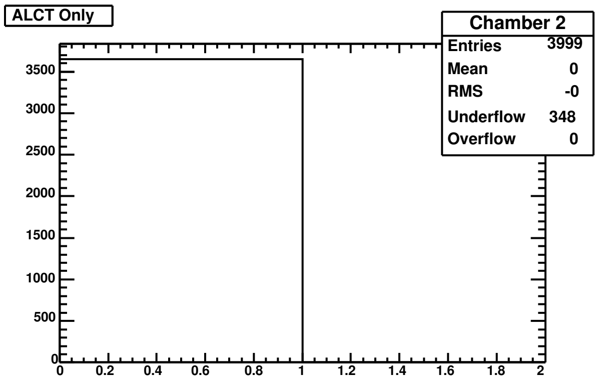

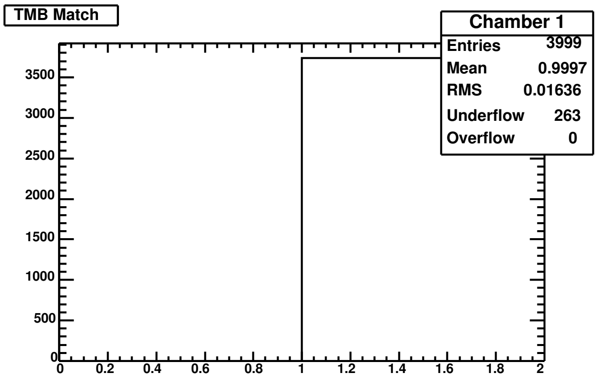

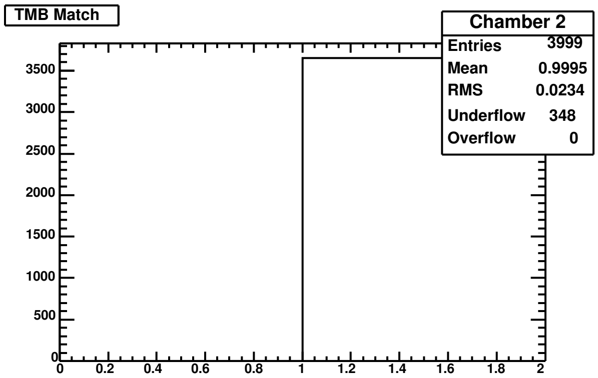

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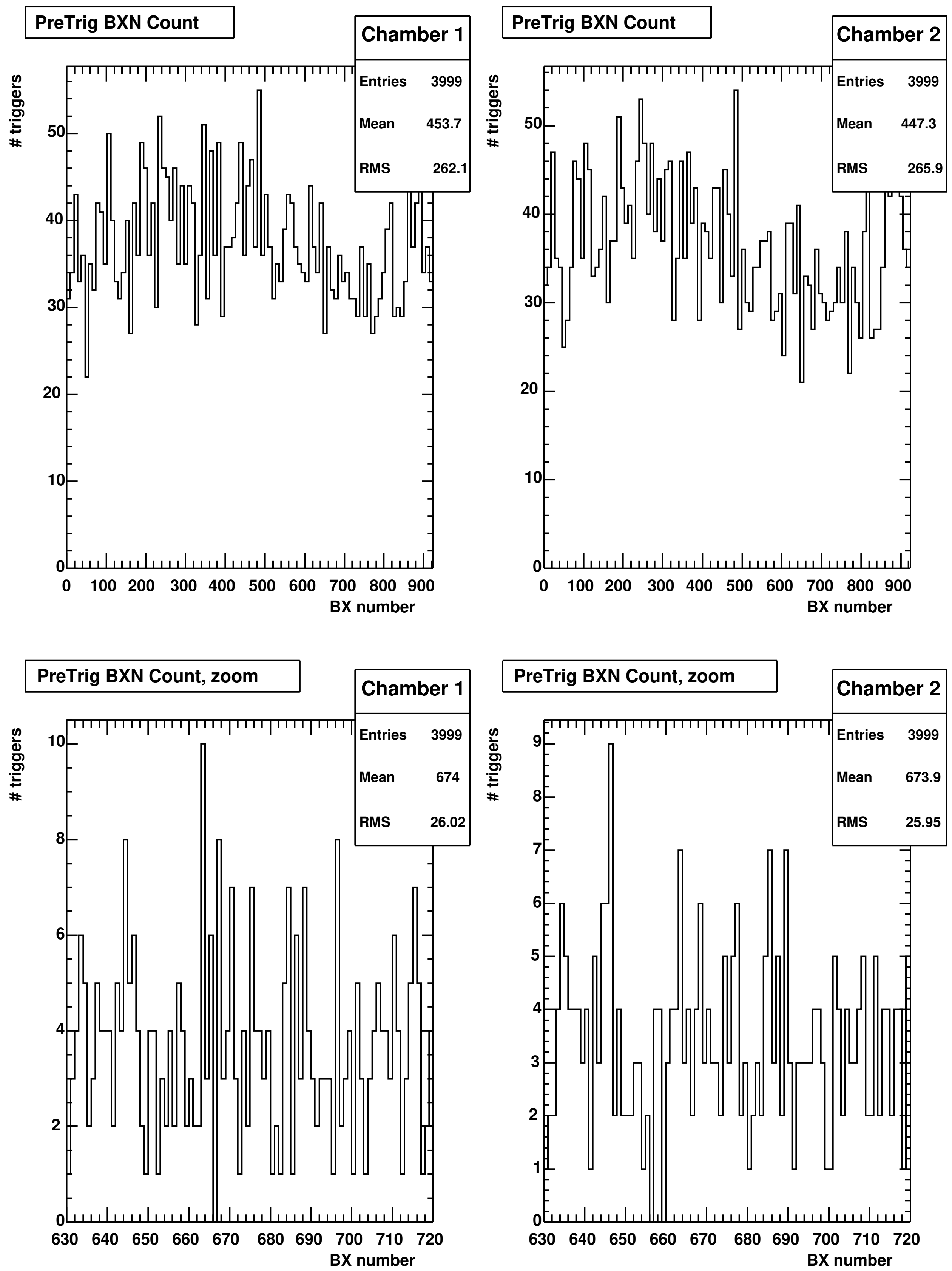
BXN Counter at Pre-trigger



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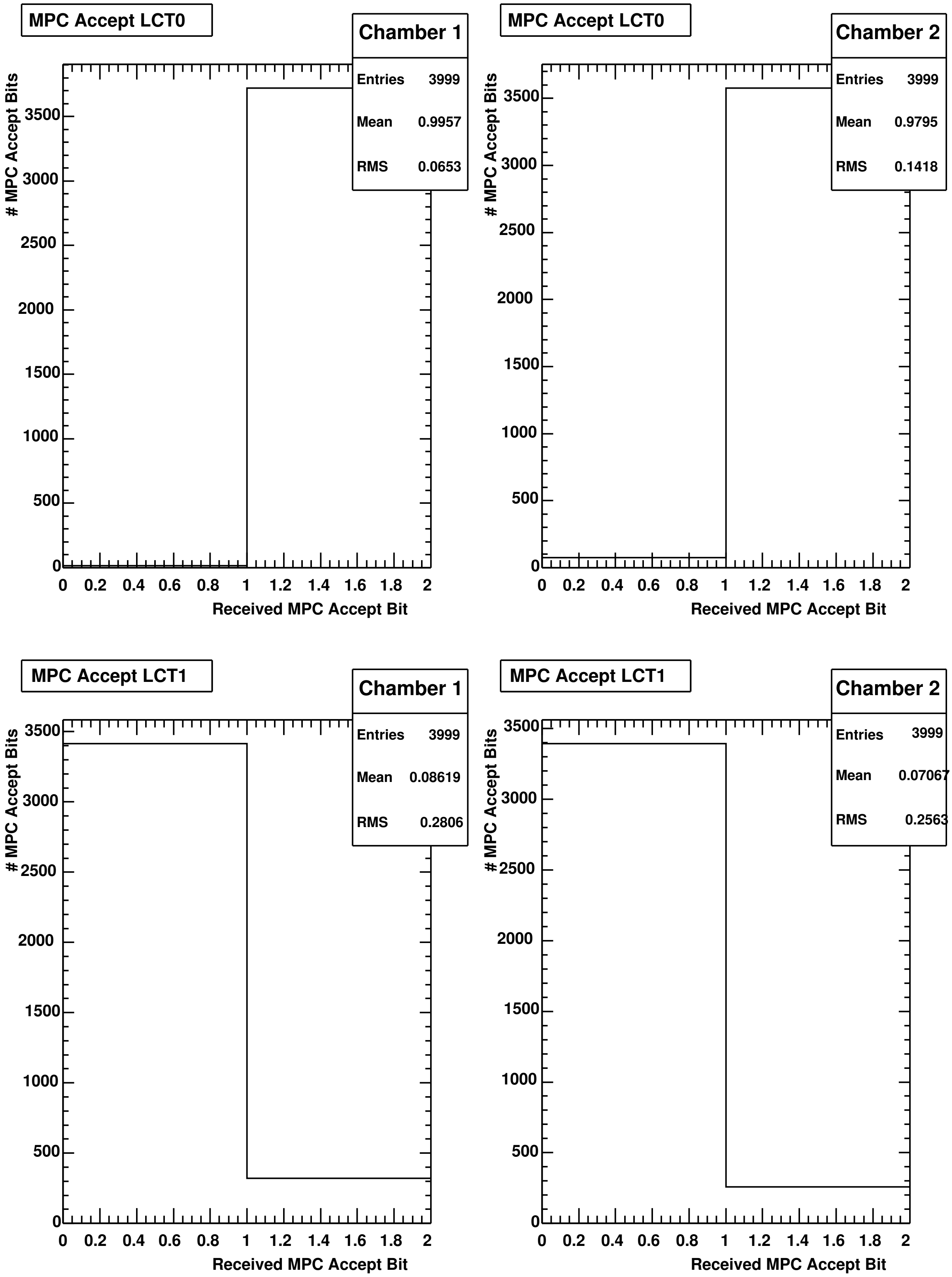
MPC Accept Bits



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