## Typical Problems of direct RC and RL circuits

Quite often, the problem likes to ask you the asymptotic behavior of the RC or RL circuits with several resistors. In those cases, you can not naively apply the simple formula of RC or RL circuits if those resistors are not just in series with the capacitor or the inductor. However, physical understanding of capacitance and inductance should make you solve those problems easily. In this article, I give you two typical examples, one on the RC circuit, and the other on the RL circuit. Normally, the problem will just ask you one part of them.

1. For the RC circuit in the figure, $R_{1}=12.0 k \Omega$ and $R_{3}=3.00 k \Omega$. The currents in $R_{1}, R_{2}$, and $R_{3}$ are denoted as $I_{1}, I_{2}$, and $I_{3}$, respectively. The charge on the capacitor is denoted as $Q$, and the voltage across the capacitor is denoted as $V_{c}$. Suppose that initially there is no charge on the capacitor, and the switch is open.
(a) Close the switch, and find $I_{1}, I_{2}, I_{3}, Q$, and $V_{c}$ immediately after the switch is closed.
(b) After the switch is closed for a length of time sufficiently long for the capacitor to become fully charged, find $I_{1}, I_{2}, I_{3}, Q$, and $V_{c}$.
(c) The switch is then reopened. Find $I_{1}, I_{2}, I_{3}, Q$, and $V_{c}$ immediately after the switch is reopened.
(d) After the switch is reopened for a length of time sufficiently long for the capacitor to become fully discharged, find $I_{1}, I_{2}, I_{3}, Q$, and $V_{c}$.


## Solution:

(a) i. The capacitor just starts to be charged $\Rightarrow Q=0$.
ii. $V_{c}=Q / C=0$.
iii. $V_{c}=0$ ( no voltage drop accross the capacitor ), and therefore, the circuit is $R_{1}\left(R_{2} \| R_{3}\right)\left(R_{2}\right.$ and $R_{3}$ are in parallel, and then they are in series with $R_{1}$ ). Hence,

$$
R_{e q}=R_{1}+\frac{R_{2} R_{3}}{R_{2}+R_{3}}=12+2.5=14.5(k \Omega)
$$

Hence,

$$
I_{1}=I_{e q}=\frac{9}{R_{e q}} \approx 0.62(m A) .
$$

iv.

$$
V_{2}=V_{3}=I_{e q} \frac{R_{2} R_{3}}{R_{2}+R_{3}}=I_{1} \frac{R_{2} R_{3}}{R_{2}+R_{3}} .
$$

Therefore,

$$
I_{2}=V_{2} / R_{2}=I_{1} \frac{R_{3}}{R_{2}+R_{3}} \approx 0.10(m A)
$$

and

$$
I_{3}=V_{3} / R_{3}=I_{1} \frac{R_{2}}{R_{2}+R_{3}} \approx 0.52(m A)
$$

(b) i. After the switch is closed for a length of time sufficiently long, the capacitor becomes fully charged and therefore the current through the capacitor should be zero ( Otherwise, it's still charging ). Hence,

$$
I_{3}=0
$$

because the current through $R_{3}$ should be the same as the current through the capacitor ( The capacitor and the $R_{3}$ are in series ).
ii. By the junction rule,

$$
I_{1}=I_{2}+I_{3}=I_{2}
$$

Hence, by the loop rule,

$$
9=I_{1}\left(R_{1}+R_{2}\right)=27 I_{1} . \Rightarrow I_{1}=I_{2}=\frac{9}{27}=\frac{1}{3} \approx 0.33(m A) .
$$

iii. The voltage $V_{2}$ across $R_{2}$ is given by

$$
V_{2}=I_{2} R_{2}=\frac{1}{3} \times 15=5(V)
$$

It is in parallel with "the capacitor and $R_{3}$ in series". Therefore,

$$
V_{2}=V_{c}+V_{3}=V_{c} \quad \Rightarrow \quad V_{3}=5(V),
$$

where I have used the fact that $V_{3}=I_{3} R_{3}=0$, since $I_{3}=0$.
iv.

$$
Q=C V_{c}=10 \times 5=50(\mu C)
$$

(c) i. Once the switch is open, the left-hand side circuit is open and therefore $I_{1}=0$.
ii. The charge was fully charged to be $50 \mu C$ before the switch is reopened. The charge on the capacitor can only change continuously, as being opposed to the current that can suddenly drop to be zero once the circuit is open. Therefore, $Q$ remains $50 \mu C$ immediately after the switch is reopened.
iii. $V_{c}=Q / C$. Therefore, $V_{c}=5(V)$, the same as in Part (b).
iv. The right hand side is still a closed circult. It's that $R_{2}, R_{3}$, and $C$ are in series. It is obvious that $I_{2}=I_{3}$ since $I_{1}=0$. Therefore, by the loop rule,

$$
V_{c}=I_{2} R_{2}+I_{3} R_{3}=I_{2}\left(R_{2}+R_{3}\right)=18 I_{2}
$$

Hence,

$$
I_{2}=I_{3}=V_{c} / 18=5 / 18 \approx 0.28(m A) .
$$

(d) i. $I_{1}=0$ ( The same reason as in Part (c) ).
ii. After the switch is reopened for a length of time sufficiently long for the capacitor to become fully discharged, $Q=0$, and therefore $V_{c}=Q / C=0$ as well.
iii. $V_{c}=0$, and thus $I_{2}=I_{3}=0$ (loop rule. Check Part(c) ).
2. This problem is almost the same as the first problem except that the capacitor is replaced by the inductor. However, the answers will be totally different.
For the RL circuit in the figure, $R_{1}=4 \Omega$ (leftmost one in the figure), $R_{2}=4 \Omega$ ( middle in the figure), and $R_{3}=8 \Omega$. The currents in $R_{1}, R_{2}$, and $R_{3}$ are denoted as $I_{1}, I_{2}$, and $I_{3}$, respectively. The voltage drops accross $R_{1}, R_{2}, R_{3}$ and the inductance $L$ are denoted as $V_{1}, V_{2}, V_{3}$, and $V_{L}$, respectively. Suppose that initially there is no current in the inductor, and the switch is open.
(a) Close the switch, and find $I_{1}, I_{2}, I_{3}$, and $V_{L}$ immediately after the switch is closed.
(b) After the switch is closed for a very long time, find $I_{1}, I_{2}, I_{3}$, and $V_{L}$.
(c) The switch is then reopened. Find $I_{1}, I_{2}, I_{3}$, and $V_{L}$ immediately after the switch is reopened.
(d) After the switch is reopened for a very long time, find $I_{1}, I_{2}, I_{3}$, and $V_{L}$.


Solution:
(a) i. The inductor is against the change of current (Lenz's law ) $\Rightarrow I_{3}=0$.
ii. Since $I_{3}=0, I_{1}=I_{2}$. Therefore, by the Kirchoff's loop rule,

$$
\varepsilon-I_{1} R_{1}-I_{2} R_{2}=0 \Rightarrow 10-8 I_{1}=0
$$

Hence, $I_{1}=I_{2}=10 / 8=1.25(\mathrm{~A})$.
iii. $L$ and $R_{3}$ are in series, and then they are in parallel with $R_{2}$. Therefore,

$$
V_{L}+V_{3}=V_{2}
$$

However, $V_{3}=I_{3} R_{3}=0$. We get $V_{L}=V_{2}=I_{2} R_{2}=1.25 \times 4=5(\mathrm{~V})$.
(b) i. After the switch is closed for a very long time, it reaches a steady state which means the current is steady, no changing with respect to time. Therefore

$$
V_{L}=L \frac{d I_{3}}{d t}=0 . \quad \text { no voltage drop accross the inductor }
$$

Or if you prefer, $V_{L}=L \Delta I_{3} / \Delta t=0$.
Therefore, the circuit is $R_{1}\left(R_{2} \| R_{3}\right)$ ( $R_{2}$ and $R_{3}$ are in parallel, and then they are in series with $R_{1}$ ). Hence,

$$
R_{e q}=R_{1}+\frac{R_{2} R_{3}}{R_{2}+R_{3}}=4+4 \times 8 /(4+8)=20 / 3(\Omega)
$$

Hence,

$$
I_{1}=I_{e q}=\frac{10}{R_{e q}}=1.5(A)
$$

ii. $V_{2}=V_{3}+V_{L}=V_{3}$.

$$
V_{2}=V_{3}=I_{e q} \frac{R_{2} R_{3}}{R_{2}+R_{3}}=I_{1} \frac{R_{2} R_{3}}{R_{2}+R_{3}}
$$

Therefore,

$$
I_{2}=V_{2} / R_{2}=I_{1} \frac{R_{3}}{R_{2}+R_{3}}=1.0(A)
$$

and

$$
I_{3}=V_{3} / R_{3}=I_{1} \frac{R_{2}}{R_{2}+R_{3}}=0.5(A)
$$

(c) i. Once the switch is open, the left-hand side circuit is open and therefore $I_{1}=0$.
ii. The current in $L$ is $I_{3}=0.5 \mathrm{~A}$ before the switch is reopened. The current in the inductor can only change continuously in the inductor: the inductor is against the change of current through it. This is opposed to the current $I_{1}$ that can suddenly drop to be zero once the circuit is open. Therefore, $I_{3}$ remains 0.5 A immediately after the switch is reopened.
Note: Through the capacitor, the voltage or change change continuously, but the current can change discontinuously ( Check part(a) of Problem 1 for example. The current suddenly changes from zero to 0.52 mA ). However, through the inductor, the current changes continuously but the voltage can change discontinuously (See the following. $V_{L}$ suddenly changes from zero to 6 V ).
iii. The right hand side is still a closed circult. It's that $R_{2}, R_{3}$, and $L$ are in series. It is obvious that $I_{2}=I_{3}$ since $I_{1}=0$. Therefore, by the loop rule,

$$
V_{L}=I_{2} R_{2}+I_{3} R_{3}=I_{3}\left(R_{2}+R_{3}\right)=0.5 \times 12=6
$$

(d) i. $I_{1}=0$ ( The same reason as in Part (c) ).
ii. After the switch is reopened for a very long time, the current becomes steady. Therefore, $V_{L}=0$.
iii. $I_{3}=I_{2}=0$ ( If it's non-zero, it means there is still power dissipation in the resistors, which will decrease the energy and thus the current of the system. $\Rightarrow$ it has not become a steady state yet. ).

