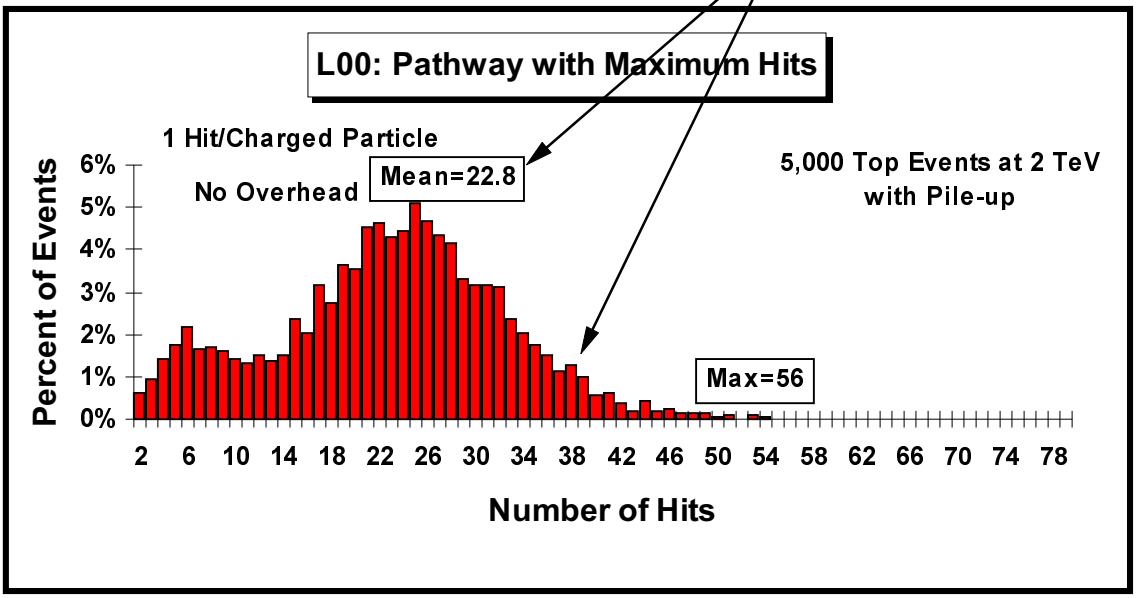
L00: Maximum Pathway Hits

One Event at 2 TeV (Event #1):

Reaction:	Top	Min-Bias	Min-Bias	Min-Bias	Min-Bias	Min-Bias
Z(int):	28.87	-30.42	21.9	-19.93	17.3	-73.45

L00	Number of Pathways			
a	b	c	d	sum
8	8	8	8	32
L00	**Number of Chips/Pathways**			
a	b	c	d	ave
4	4	4	4	4
L00	**Number of Pathways Hit**		**Event #1**	
a	b	c	d	sum
7	7	8	8	30
L00	**Number of Pathway Hits**		**Event #1**	
a	b	c	d	sum
27	21	52	134	234
L00	**Maximum Pathway Hits**		**Event #1**	
a	b	c	d	all
5	6	8	38	38

5,000 Top Events at 2 TeV (<Min-Bias>=3, σ_Z=30 cm):

L00	Average Max Pathway Hits		5,000 Top Events	
a	b	c	d	all
8.3	11.3	11.2	8.3	22.8
L00	**Ave Max Pathway Occupancy**		**5,000 Top Events**	
a	b	c	d	all
4.9%	6.6%	6.6%	4.9%	13.4%