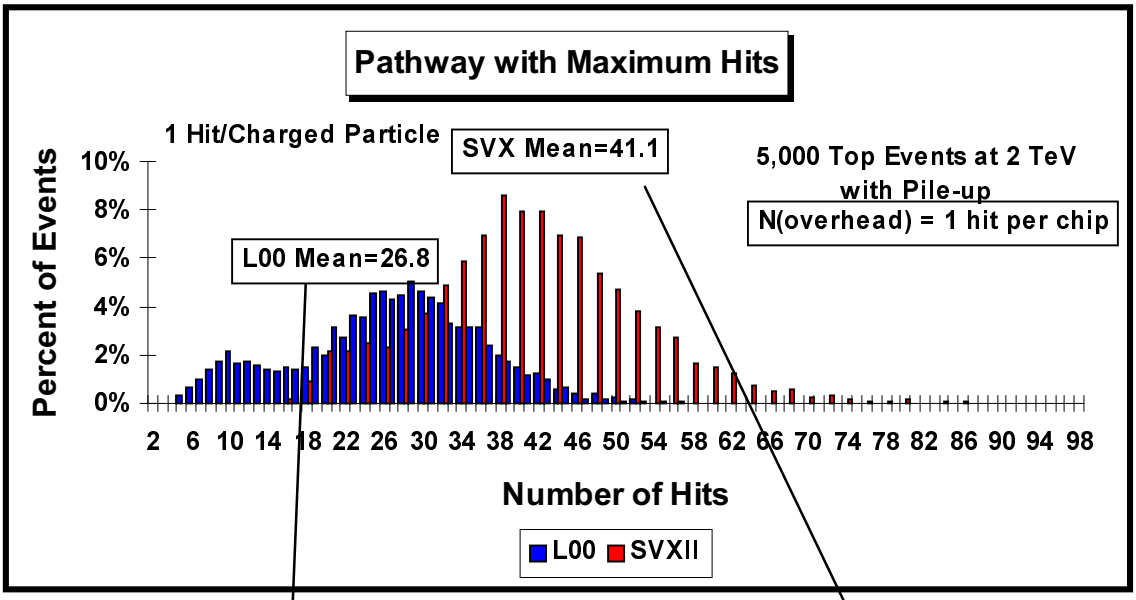
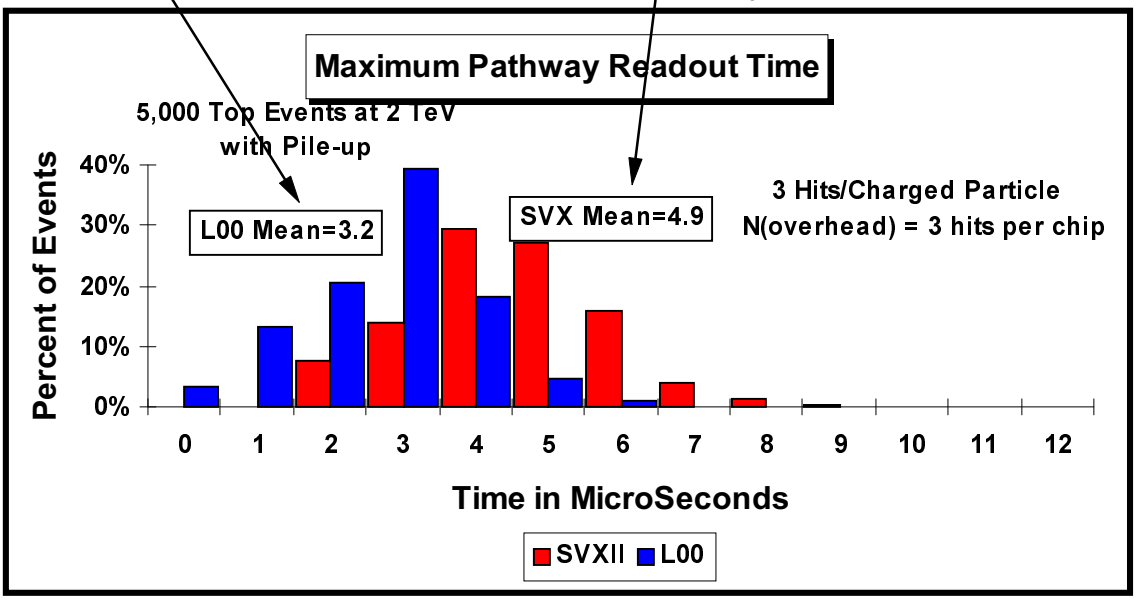
SVXII & L00: Maximum Pathway Hits

5,000 Top Events at 2 TeV (<Min-Bias>=3, σ_z=30 cm):

With an added "overhead" of 1 hit per chip



Nearest Neighbor Hits

3.2μs = 3*26.8/25MHz **4.9μs = 3*41.1/25MHz**

SVXII & L00: Maximum Pathway Readout Time