Configuration of the Track Finder Test Station

1. **Adapter Card**
   - PCI-VMEdbus Adapter (Model 616, “SBS Bit3 Operations”)

2. **Prototype Board**
   - 9U board with real input and output data

3. **Tester Board**
   - Loadable RAM for test pattern generation to the Prototype Board inputs
   - Track-finder algorithm simulation
   - RAM to receive output information from the Prototype Board
EU – Extrapolation Unit  
QSU – Quality Selector Unit  
TAU – Track Assembler Unit  
FSU – Final Selector Unit
Block Diagram of Extrapolation Unit